INSTRUCTION MANUAL

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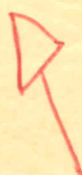
TYPE 1650-A
IMPEDANCE BRIDGE

1650

GENERAL RADIO COMPANY

OPERATING INSTRUCTIONS

TYPE 1650-A

IMPEDANCE BRIDGE

Form 1650-0100-H
ID1079
May, 1966

GENERAL RADIO COMPANY

WEST CONCORD, MASSACHUSETTS, USA

TABLE OF CONTENTS

SPECIFICATIONS

RANGES OF MEASUREMENT

Resistance: 1 milliohm to 11 megohms, eight ranges, ac or dc.

Capacitance: 1 pF to 1100 μF, seven ranges, series or parallel.

Inductance: 1 μH to 1100 H, seven ranges, series or parallel.

D: (of series capacitance) – 0.001 to 1 at 1 kc/s.
   (of parallel capacitance) – 0.1 to 50 at 1 kc/s.
   ($C_s = C_p$ within 1% if D < 0.1.)

Q: (of series inductance) – 0.02 to 10 at 1 kc/s.
   (of parallel inductance) – 1 to 1000 at 1 kc/s.
   ($L_s = L_p$ within 1% if Q > 10.)

ACCURACY

AC Resistance: ±1% ±1 milliohm (residual R = 1 milliohm).

DC Resistance: 1% from 1 ohm to 100 kilohms. An external dc supply is required for 1% accuracy above 100 kilohms.

Capacitance: ±1% ±1 pF (residual C = 0.5 pF).

Inductance: ±1% ±1 μH (residual L < 0.2 μH).

D: ±5% ±0.001 at 1 kc/s or lower, ±0.001f/1kc above 1 kc/s.

1/Q: ±5% ±0.001 at 1 kc/s or lower, ±0.001f/1 kc above 1 kc/s.

Frequency: (1 kc/s supplied internally) 1% accuracy for R, 20 cp/s to 5 kc/s; for L and C, 20 cp/s to 20 kc/s. D and Q ranges are functions of frequency. With reduced accuracy, measurements can be made up to 100 kc/s.

U. S. Patent Nos 2,872,639 and 2,966,257.
GENERAL RADIO EXPERIMENTER reference: Volume 33, No. 3, March 1959; Volume 33, No. 4, April 1959. Available combined as General Radio Reprint No. E-108.

GENERATOR AND DETECTOR

Internal Oscillator Frequency (external ac and dc sources can also be used): 1 kc/s ±2%.

Internal Detector: Response, flat or selective at 1 kc/s; sensitivity control provided.

Internal DC Supply: 6 volts, 60 milliamperes, maximum.

Power Required: Portable model, 4 D cells supplied; rack model, rechargeable, 6-volt, nickel-cadmium battery, supplied. Battery capacity is adequate for 40 hours of use in ac measurements, 8 hours minimum in dc measurements. Charging time, 10 hours.

External Oscillator and Detector: Type 1310-A or Type 1311-A Oscillator, and Type 1232-A Null Detector are recommended for audio measurements at frequencies other than 1 kc/s.

DC Polarization: 600 volts may be applied (from external source) for series capacitance measurements.

GENERAL

Accessories Available: Type 1650-P1 Test Jig.

Other Accessories Required: None. Earphones can be used for high precision at the extremes of the bridge ranges.

Cabinet: Flip-Tilt; relay-rack models also available.

Dimensions: Portable model, case closed – width 12¾, height 12½, depth 7¾ inches (325 by 320 by 200 mm), over-all; rack model – panel 19 by 12¼ inches (485 by 315 mm); depth behind panel 5 inches (130 mm).

Net Weight: Portable model, 17 pounds (8 kg); rack model, 18 pounds (8.5 kg).

Shipping Weight: Portable model, 21 pounds (10 kg); rack model, 30 pounds (13.6 kg).

TYPE 1650-P1 TEST JIG

This test-jig adaptor provides a way to connect components quickly to a pair of terminals, which can be placed on the bench directly in front of the operator. Thus, the test jig and TYPE 1650-A Impedance Bridge make a rapid and efficient component sorting device when the panel meter of the bridge is used as a limit indicator.

The test jig makes a three-terminal connection to the bridge, so that the residual zero capacitance is negligible. The lead resistance (0.08 ohm total) has effect only when very low impedances are measured, and the lead capacitance affects only the measurement of the Q of inductors, introducing a small error in $D \left(\text{or } \dfrac{1}{Q}\right)$ of less than 0.007.

Net Weight: 10 ounces (285 grams).

Shipping Weight: 4 pounds (1.9 kg).
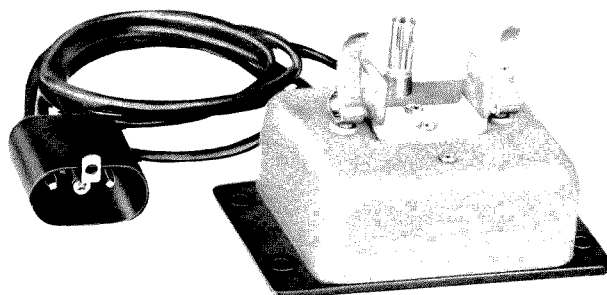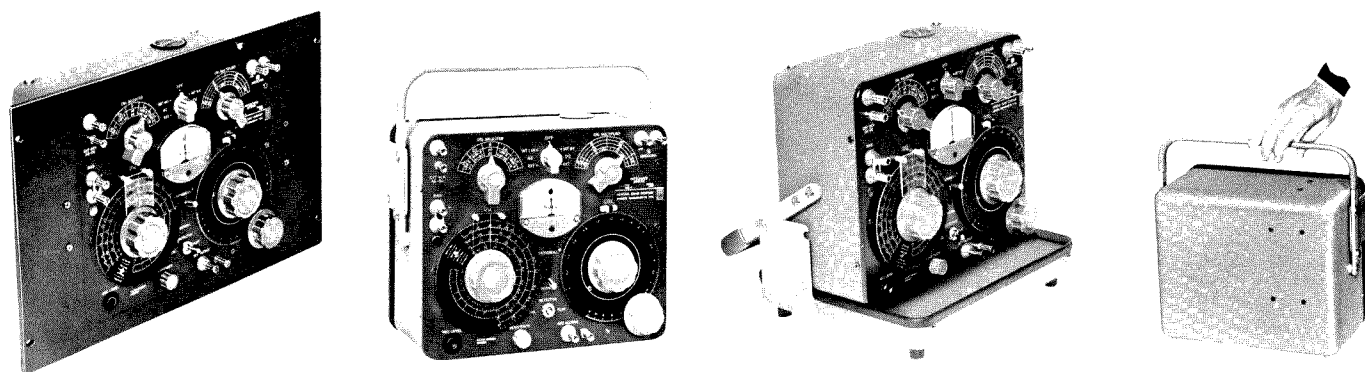
Figure 1.
Type 1650-A Impedance Bridge.
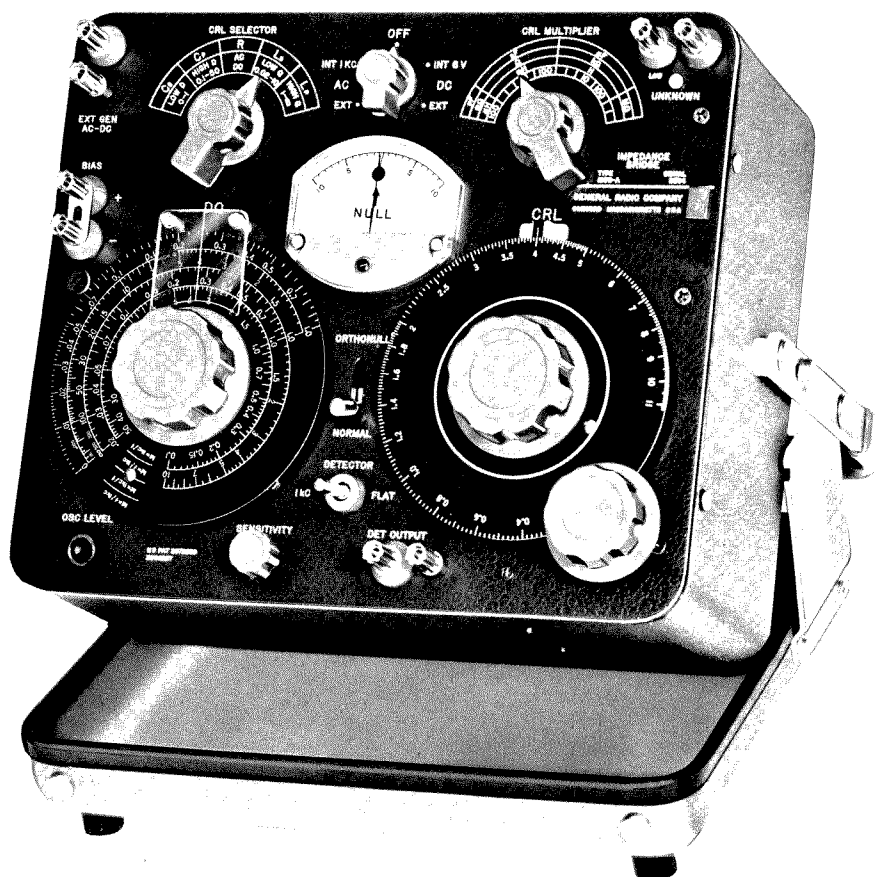
SECTION 1

INTRODUCTION

1.1 DESCRIPTION.

1.1.1 GENERAL. The Type 1650-A Impedance Bridge (Figure 1) is a self-contained impedance-measuring system, which includes five bridges for the measurement of capacitance, resistance, and inductance, as well as the generators and detectors necessary for dc and 1-kc ac measurements. Features of this bridge include one-percent C, R, and L accuracy over all ranges, high D and Q accuracy, a mechanism to facilitate low Q measurement, visual ac and dc null indications, complete portability, and a convenient tilting mechanism and carrying case.

Two relay-rack models of the Type 1650-A Impedance Bridge are available, the Type 1650-9540 for 115-volt operation and the Type 1650-9570 for 230-volt operation. In the relay-rack models, the captive cover of the Type 1650-A is replaced with a relay-rack adaptor panel and the dry-cell battery is replaced with a rechargeable battery assembly.

1.1.2 CONTROLS. The Table of Controls given below lists the controls located on the front panel of the Type 1650-A Impedance Bridge.

1.1.3 CONNECTORS. The Table of Connectors given below lists the connectors located on the front panel of the Type 1650-A Impedance Bridge.

TABLE OF CONTROLS

| Name	No.	Type	Function
CRL MULTIPLIER	S1	8-position selector switch	Selects impedance range.
CRL SELECTOR	S2	5-position selector switch	Selects bridge circuit.
Function Switch	S3	5-position selector switch	Turns bridge on, to type of operation required.
CRL Dial	R1	Continuous rotary control	Adjusts for bridge balance.
DQ Dial	R2	Continuous rotary control	Adjusts for bridge balance.
ORTHONULL® Lever		Mechanical lever	Engages Orthonull mechanism.
DETECTOR Switch	S4	Toggle switch	Controls detector response.
OSC LEVEL	R18	Thumbset rotary control	Controls ac oscillator level.
SENSITIVITY	R15 R16	Continuous rotary control	Controls ac and dc detector sensitivity.
Rack models only: CHARGE-MEASURE		Toggle switch	Connects battery assembly to power line for recharging.

TABLE OF CONNECTORS

Name	No.	Type	Function
UNKNOWN	J7, J8	Jack-top binding-post pair	Connects unknown impedance.
EXT GEN	J1, J2	Jack-top binding-post pair	Connects ac or dc external source.
BIAS	J3, J4	Jack-top binding-post pair	Connects dc bias.
DET OUTPUT	J5, J6	Jack-top binding-post pair	Connects external amplifier or phones.

1.2 SYMBOLS, ABBREVIATIONS, AND DEFINITIONS.
The following symbols, abbreviations, and definitions are used on the panel of the Type 1650-A and this instruction manual:

C   capacitance ($\dashv\vdash$)

C_s series capacitance

C_p parallel capacitance

L   inductance

L_s series inductance

L_p parallel inductance

R   resistance, the real part of an impedance

R_s series resistance

R_p parallel resistance

X   reactance, the imaginary part of an impedance

Z   impedance

Q   quality factor $= \dfrac{X}{R} = \dfrac{1}{D}$

for inductors $\dfrac{\omega L_s}{R_s} = \dfrac{R_p}{\omega L_p}$

D   dissipation factor $= \dfrac{R}{X} = \dfrac{1}{Q}$

for capacitors $\omega c_s R_s = \dfrac{1}{\omega C_p R_p}$

PF   power factor $= \dfrac{R}{|Z|} = \dfrac{R}{\sqrt{R^2 + X^2}}$

f   frequency

ω angular frequency $2\pi f$

Ω ohm, a unit of resistance, reactance, or impedance

$k\Omega$ kilohm, 1 $k\Omega$ = 1000 ohms

M   multiplying factor applied to D and Q at frequencies other than 1 kc

$M\Omega$ megohm 1 $M\Omega = 1 \times 10^6$ ohms

μf microfarad, a unit of capacitance

$\mu\mu f$ (or pf) micromicrofarad (or picofarad) $1\mu\mu f = 1pf = 1 \times 10^{-6}\mu f$

$m\mu f$ (or nf) millimicrofarad (or nanofarad) $1m\mu f = 1nf = 0.001\ \mu f$

$m\Omega$ milliohm 1 $m\Omega$ = 0.001 ohm

nf   (or $m\mu f$) nanofarad (or millimicrofarad) $1nf = 1m\mu f = 0.001\ \mu f$

pf   (or $\mu\mu f$) picofarad (or micromicrofarad) $1pf = 1\mu\mu f = 1 \times 10^{-6}\mu f$

h   henry, a unit of inductance

mh   millihenry 1 mh = 0.001 h

μh microhenry 1 $\mu h = 1 \times 10^{-6}$ h

1.3 SERIES AND PARALLEL COMPONENTS.
An impedance that is neither a pure reactance or a
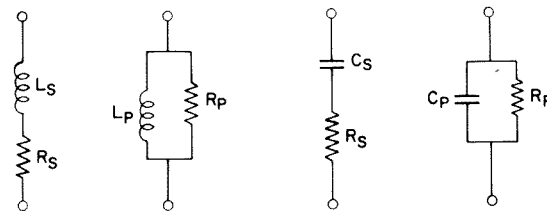
Figure 2. Equivalent Circuits for Complex Impedance.

pure resistance may be represented at any specific frequency by either a series or a parallel combination of resistance and reactance. The values of resistance and reactance used in the equivalent circuit depend on whether a series or a parallel combination is used. The equivalent circuits are shown in Figure 2. A nomograph for series-parallel conversion is given in Appendix A.

The relationships between the circuit elements are:

Resistance and Inductance

$$Z = R_s + j\omega L_s = \frac{j\omega L_p R_p}{R_p + j\omega L_p} = \frac{R_p + jQ^2\omega L_p}{1 + Q^2}$$

$$Q = \frac{1}{D} = \frac{\omega L_s}{R_s} = \frac{R_p}{\omega L_p}$$

$$L_s = \frac{Q^2}{1 + Q^2}L_p = \frac{1}{1 + D^2}L_p$$

$$L_p = \frac{1 + Q^2}{Q^2}L_s = (1 + D^2)L_s$$

$$R_s = \frac{1}{1 + Q^2}R_p; \quad R_p = (1 + Q^2)R_s$$

$$R_s = \frac{\omega L_s}{Q}; \quad R_p = Q\omega L_p$$

Resistance and Capacitance

$$Z = R_s + \frac{1}{j\omega C_s} = \frac{\dfrac{R_p}{j\omega C_p}}{R_p + \dfrac{1}{j\omega C_p}} = \frac{D^2 R_p + \dfrac{1}{j\omega C_p}}{1 + D^2}$$

$$D = \frac{1}{Q} = \omega R_s C_s = \frac{1}{\omega R_p C_p}$$

$$C_s = (1 + D^2)C_p; \quad C_p = \frac{1}{1 + D^2}C_s$$

$$R_s = \frac{D^2}{1 + D^2}R_p; \quad R_p = \frac{1 + D^2}{D^2}R_s$$

$$R_s = \frac{D}{\omega C_s}; \quad R_p = \frac{1}{\omega C_p D}$$

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SECTION 2

PRINCIPLES OF OPERATION

2.1 GENERAL. Figure 3 shows the five bridge circuits used in the Type 1650-A Impedance Bridge, as well as the balance equations. Hays and Maxwell inductance bridges and series and parallel capacitance comparison bridges are used to provide wide coverage over the D and Q ranges, as shown in Figure 4. Full use of these wide ranges at low Q and high D values is achieved by means of an Orthonull® balancing mechanism (refer to paragraph 2.5). Both ac and dc measurements may be made with the bridge, which has no internal phase balance.

The variable bridge components are General Radio precision wire-wound rheostats. The CRL rheostat uses a mechanical justifying mechanism for high accuracy, and the DQ rheostat has a 54-db logarithmic range. The standard capacitor is a General Radio Type 505 silvered-mica capacitor,
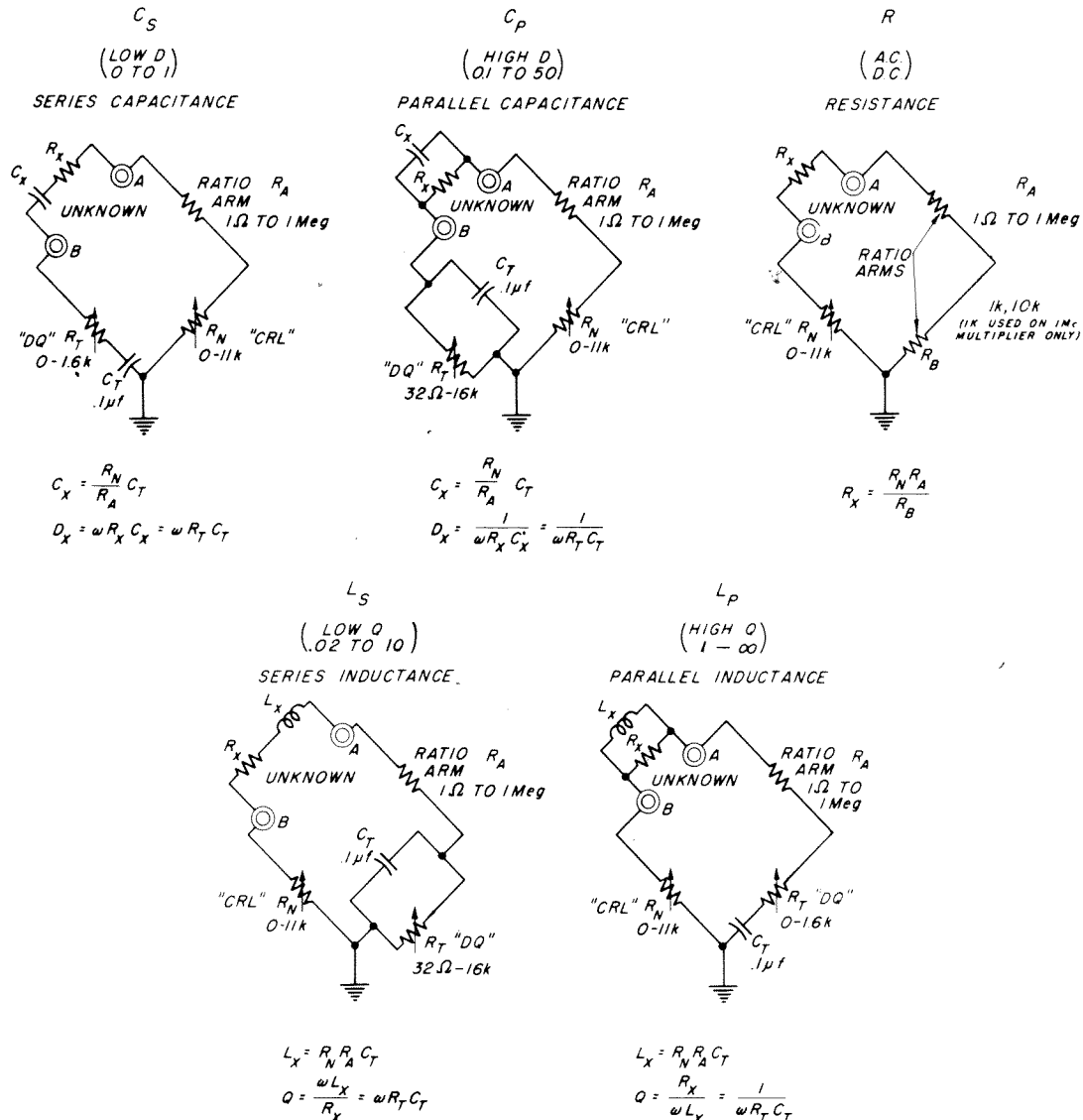
Figure 3. Bridge Circuits Used in Impedance Bridge.
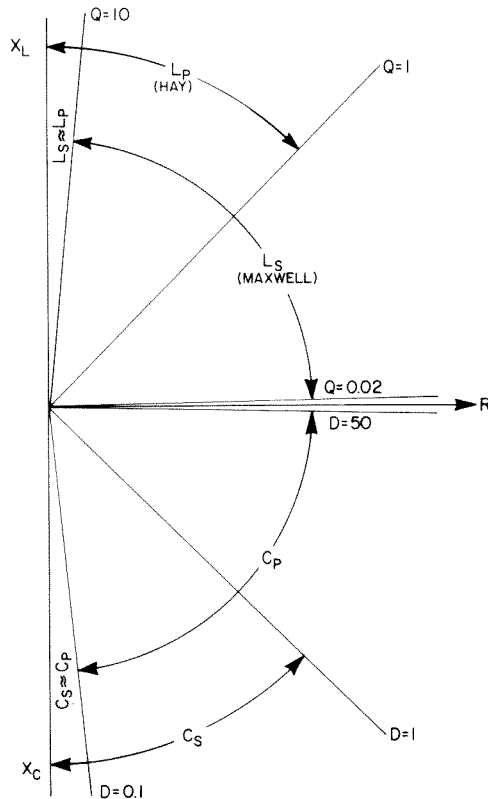
Figure 4. DQ Coverage Chart.

Figure 5. Source and Detector Diagrams.

and the resistors are General Radio wire-wound cards except for the 1-megohm ratio arm, which uses a 1/4% precision film resistor.

2.2 BRIDGE SWITCHING. The CRL MULTIPLIER switch (S1) selects the bridge range by switching in various ratio-arm resistors. Clockwise rotation of this two-rotor switch increases the multiplier value for the R, L, and C bridges. Both ends of the range resistor are switched out so that the unused resistors may be grounded to reduce capacitance across this arm. Double, solid silver contacts insure low switch resistance and long switch life.

The CRL SELECTOR switch (S2) switches the bridge circuits. The actions of this switch are such that it (1) selects the correct rotors of S1 and grounds one of the unused rotors, (2) selects the correct standard arm, and (3) reverses the bottom two arms of the bridge to form the L and R or C bridges.

The function switch (see Figure 5) sets up the correct internal source and detector circuits for the desired operation. When this switch is in either of the two EXT positions, the EXT GEN terminals, used for externally applied ac or dc, are connected in as the bridge source.

2.3 COMPENSATION TECHNIQUES. To achieve the required D-Q accuracy over such wide ranges,

several compensating schemes are used. The components used for this purpose are listed below, with brief description of their functions. Component designations refer to Figure 22.

C2 and L1: These components are used to make the standard resistance arm (R_b, Figure 3) appear resistive over a wide frequency range. This arm is shunted with considerable stray capacitance, which, without compensation, would cause a poor ac null

and an error. The resistances of L1 and R4 add up to the required 10 kilohms.

C3: This capacitor corrects the phase angle of the first section of the DQ potentiometer (R_t) to compensate for the inductance of the winding. Without compensation, this inductance would cause an error in C_s and L_p at high frequencies, and in C_p and L_s when the unknown has a very low Q or high D.

C4: This capacitor corrects for the phase shift caused by stray capacitance across the CRL rheostat (R_N). This capacitor forms a three-terminal T network with the two parts of the rheostat to produce an effective inductance to balance out the stray capacitance.

C5: This capacitor compensates for the stray capacitance across the 1-megohm ratio arm (R12 and R13). The three-terminal T network formed by these components produces an effective inductance to balance out the stray capacitance.

C6: This capacitor compensates for the inductance of the 1-ohm ratio arm (R5).

2.4 BRIDGE SOURCES AND DETECTORS. The dc bridge supply is taken from the four internal D cells (from the rechargeable battery in the Types 1650-9540 and -9570), which supply about 6 volts limited by a 100-ohm resistor to a maximum of 60 ma. The dc indicator on the panel has a sensitivity of $2\mu a/mm$ near zero, a resistance of 75 ohms, and a shaped characteristic (Marion Type C null indicator).

The ac source is a 1-kc transistor LC oscillator, which uses the primary of the bridge transformer as the inductor in the tuned circuit. The output voltage is about 1 volt at the secondary of the 4-to-1 step-down transformer. This secondary is wound with resistance wire to increase the resistance to about 150 ohms, preventing external loads from affecting the bridge frequency. The OSC LEVEL control adjusts output voltage by loading the transformer secondary.

The ac detector is a three-transistor, variable-gain amplifier, which uses a twin-T RC filter to obtain selectivity with the DETECTOR switch in the 1 kc position. This amplifier drives the panel meter to provide a visual ac null indication, and the output from the amplifier is supplied to the panel DET OUTPUT terminals.

The ac oscillator and detector combined draw less than 10 ma from the internal 6-volt battery.

2.5 ORTHONULL. Orthonull is a mechanical device that improves the bridge balance convergence when low Q inductors or high D capacitors are measured.

Ordinarily, balances with such components are tedious and often impossible due to the "sliding null" resulting from the interdependence of the two adjustments. Rapid balances are possible with Orthonull, which does not affect electrical balance but which does help avoid false nulls, improving bridge accuracy for low Q measurements.

The bridge output voltage for the L_s (Maxwell) bridge can be expressed:

$$\frac{E_o}{E_{in}} = \frac{R_x + j\omega L_x - \left(\dfrac{R_n R_a}{R_t} + j\omega R_n C_t R_a \right)}{\text{Denominator}} \quad (1)$$

We will assume that the denominator is more or less constant in the region of the null. The numerator is the difference between the unknown impedance $R_x + j\omega L_x$ and what can be called the "bridge impedance". The bridge output is proportional to this difference, which is the distance between them on the complex plane. To balance the bridge, the "bridge impedance" is varied by adjustment of R_n (the CRL dial) and R_t (the DQ dial) until it equals the unknown impedance. An adjustment of R_t varies only the real part of the bridge impedance, whereas an adjustment of R_n varies both parts, and is therefore a multiplier of the bridge impedance. Thus, adjustment of R_t moves the bridge impedance horizontally on the complex plane, while adjustment of R_n moves it radially (see Figure 6). Each control is adjusted for a minimum voltage.
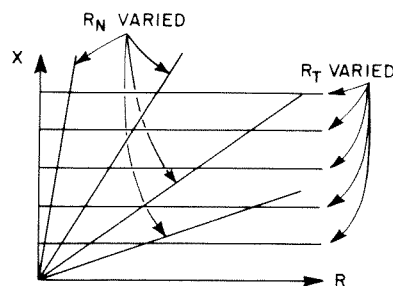
Figure 6. Loci of R_n and R_t Adjustments on Z Plane.

When X≫R (i.e. when Q is high) these two adjustments are almost orthogonal, and rapid convergence is possible. When Q is low, however, the adjustment becomes more parallel and convergence is slow, causing a "sliding null", as shown in Figure 7, where Q = 1/2. With smaller Q's, convergence is even slower.

The Orthonull device makes the two adjustments orthogonal by nonreciprocally ganging R_n and R_t. From equation (1) it is apparent that if R_n/R_t remained constant as R_n was varied, only the imaginary part of the bridge impedance would

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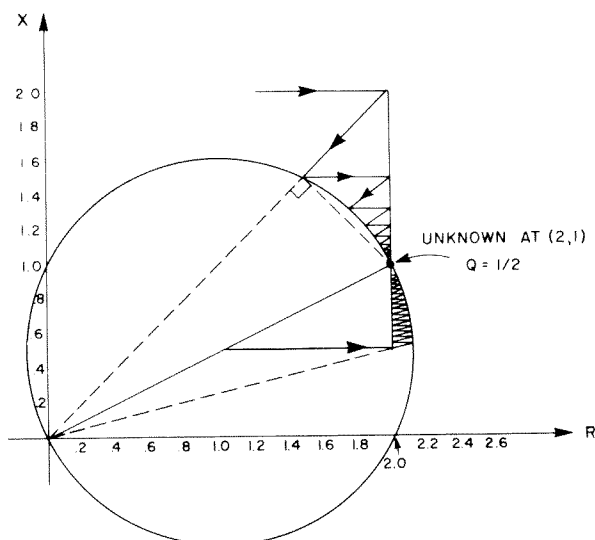
Figure 7. Loci of "Sliding Null" Balance.

Figure 8. Number of Balances vs Q.

change. But when R_t is adjusted, R_n must not move to vary only the real part. The solution is a simple friction clutch to permit nonreciprocal action. Both the inherent difference in friction of the two rheostats and the pulley ratio favor torque transmission in the desired direction.

The ratio R_n/R_t must be constant for variation in R_n for any initial settings of R_n and R_t, since R_t may be moved independently of R_n. This requires rheostats with exponential characteristics (and logarithmic dials). The DQ rheostat is a 54-db exponential potentiometer with the correct initial resistance (R3) added when the L_s and C_p bridges are used. The CRL rheostat is exponential in the dial range from 1 to 11, and linear below 1. Thus, for correct Orthonull action, the CRL dial must be in the range above 1.

The Orthonull mechanism is shown in Figure 21. The clutch material is between the pulley attached to the DQ shaft and the free pulley driven by the wire belt. The clutch is disengaged by the lever on the panel so that normal operation is possible for high Q (low D) components.

The advantage of Orthonull is illustrated in Figure 8, which is a plot of the numbers of adjustments necessary for a balance. Not only does the Orthonull reduce the number of balances, but it permits 1% measurements that would otherwise be impossible below a Q of 1/3, due to the finite resolution of the DQ rheostat. This finite resolution causes the meter indication to vary in jumps when Orthonull is used at Q's below 1/3. However, by choosing the best null, 1% accuracy is possible with Q's of less than 0.2. As Q is further reduced, it is even-

tually impossible to achieve 1% balances. The accuracy that can be expected with careful adjustment is plotted against Q in Figure 9. In the face of the fact that for low Q values

$$\frac{d|Z|}{|Z|} = Q^2 \frac{dL}{L}$$

the eventual lack of accuracy is justified. For example, if Q = 0.03, a 5% change in inductance is a change of only 45 parts per million in impedance.

As far as the user is concerned, the balancing procedure with Orthonull is essentially the same as without it. However, several suggestions for its use are given in paragraph 5.5.
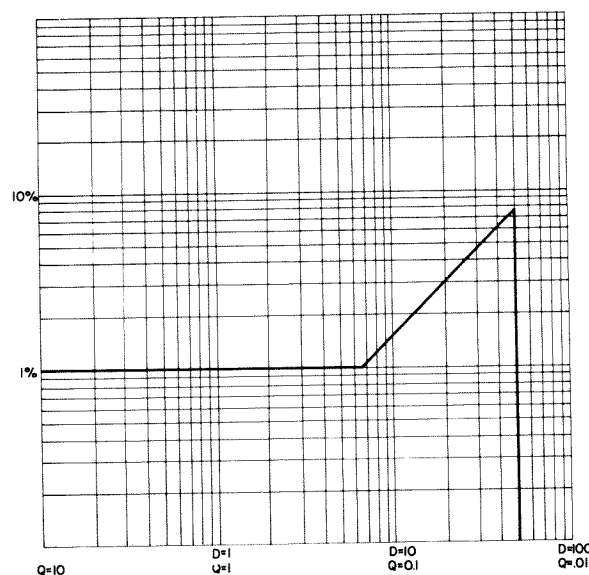
Figure 9. Accuracy vs D or Q.

SECTION 3

INSTALLATION

3.1 OPENING AND TILTING THE CABINET. The directions for opening the Type 1650-A Impedance Bridge are given on the handle support of the instrument. Once open, the instrument may be tilted to any convenient angle as shown in Figure 1. The angle should be chosen to give the most comfortable access to the knobs and the best view of the meter and dials.

The instrument may be locked fully open by the same slide pins that are used to lock the instrument closed. Thus, the instrument can be carried in the open position with the cover firmly in place.

When the instrument is open, the cover forms a convenient storage place for the instruction manual and for any other test data that should be kept with the instrument.

3.2 POWER SUPPLY. The Type 1650-A is powered by four D cells, which slide into the instrument through the cap at the top. These batteries, supplied with the instrument, should be installed with the positive terminals (center buttons) facing down. The batteries are protected from leakage and accidental discharge during shipment by a yellow insulating disk inserted between the cap and the last cell. Remove the disk. The instrument is then ready to operate as soon as it is in position and turned on.

The Types 1650-9450 and -9570 relay-rack models are powered by an encapsulated rechargeable battery assembly, which includes transformer rectifiers for charging. The battery gives approximately 40 hours of operation before it needs to be recharged. If the bridge is used continuously, it should be recharged once a week. If the bridge is used occasionally, the battery should be recharged once a month. To recharge the battery leave the CHARGE-MEASURE panel switch set at CHARGE overnight. (About 10 hours are required to recharge.) Do not leave the battery connected to the power line (panel switch set to CHARGE) continually since battery life will be decreased.

SECTION 4

OPERATING PROCEDURE —DC MEASUREMENTS

4.1 RESISTANCE MEASUREMENTS USING 6-VOLT SUPPLY.

4.1.1 PROCEDURE.

a. Check the NULL meter mechanical zero with the function switch in the OFF position, and, if necessary, center the pointer with the mechanical zero adjustment on the meter.

b. Turn the SENSITIVITY control almost fully counterclockwise.

c. Set the CRL SELECTOR to R.

d. Connect the resistor to be measured to the UNKNOWN terminals.

e. Turn the function switch to INT 6 V.

NOTE

As the function switch is rotated from OFF to INT 6 V, it passes through an undetented position where the circuit is operative but the meter is shunted to reduce sensitivity. A preliminary balance may be made with the switch in this position instead of with the SENSITIVITY control turned down.

f. Set the CRL MULTIPLIER switch and the CRL dial for a zero (center) meter reading, while adjusting the SENSITIVITY control to increase sensitivity. A meter deflection to the right indicates that the unknown is larger than the multiplier and dial setting. For greatest accuracy the final balance should be between 1 and 11 on the CRL dial (possible above 100 milliohms).

g. The value of the unknown resistance is the product of the CRL dial indication and the factor indicated on the CRL MULTIPLIER switch.

4.1.2 SENSITIVITY. With the internal 6-volt supply, one-percent balances may be easily made up to 10 kilohms and with care up to 100 kilohms. Above 100 kilohms a higher external voltage should be used (refer to paragraph 4.2). Below 1Ω, the sensitivity limits the accuracy to $\pm 10 \, m\Omega$. A more sensitive meter may be placed in series with the internal meter by placing it across the BIAS terminals.

A 100-ohm resistor in series with the internal 6-volt supply limits the current in the unknown to 60 ma. The unknown is in series with the CRL rheostat, so that the unknown current is greatest when the CRL dial is at zero.

The maximum power that can be applied to the bridge by the internal supply is 0.09 watt; thus

there is no danger of injuring components rated at 1/10 watt or more.

At range extremes it is often desirable to make 1-kc ac measurements to increase sensitivity. For most resistors, the difference between the measured 1-kc and dc values is negligible.

4.1.3 ACCURACY OF DC RESISTANCE MEASUREMENTS.

The accuracy of dc resistance measurements is ±1% if the CRL dial reading is between 1 and 11 as long as there is enough sensitivity. Below 1Ω, the accuracy is limited to ±10 mΩ by the sensitivity. Above 100 kΩ, an external supply is required to get 1% accuracy.

For low-resistance measurements, short, heavy leads should be used as connections to the unknown. The zero resistance of the leads should be measured with the free ends connected together, and subtracted from the bridge reading with the unknown in place. The user should be particularly careful when using banana-pin connections. For best connection to the bridge, screw the binding post hard enough to notch the wire inserted in the hole.

4.2 RESISTANCE MEASUREMENTS USING EXTERNAL DC SUPPLIES.

4.2.1 PROCEDURE.

The procedure for dc resistance measurements using an external supply is the same as that described in paragraph 4.1.1 except that:

a. The external supply should be connected across the EXT GEN terminals.

b. Set the function switch to the DC EXT position.

WARNING

The operator should use extreme care when using external dc supplies. It is advisable to limit high-voltage supplies to a current of 5 ma or less by placing resistance in series. Care should be taken to avoid damage to the bridge and to the unknown component.

4.2.2 VOLTAGE AND CURRENT LIMITS.

Bridge voltages must be limited to protect the bridge and the unknown component from damage. It is also advisable to limit the current to 5 ma or less to protect the operator from injury. The maximum voltage limit and standard EIA test voltages are described below.

Unless the utmost in sensitivity or a standard test voltage is desired, a supply of about 100 volts (e.g. a 90-volt battery), with about 25 kilohms in series, is recommended. The available power from such a supply is 0.1 watt, which is a low enough dissipation for almost all resistors, and the maximum current is 4 ma. Such a supply permits measurements up to 1 megohm with 1% accuracy. For resistances over 1 megohm, a higher voltage is desirable for good sensitivity, but it should be noted that the maximum EIA test voltage is 100 volts, and that various types of resistors have different voltage ratings.

The maximum voltage and current that may be applied to the bridge for each range are given in Table 1. Careful observation of both of these limits will prevent damage to the bridge.

Because the full voltage may be applied to the unknown, it is advisable to limit the available power to a value less than the power rating of the unknown component.

Various EIA standards for testing different types of resistors are summarized in Tables 2 and

TABLE 1
MAXIMUM DC BRIDGE VOLTAGE
AND CURRENT

Range Full Scale	Range Multiplier	E Max	I* Max
1 Ω	100 mΩ	71 v	100 ma
10 Ω	1 Ω	71 v	100 ma
100 Ω	10 Ω	71 v	71 ma
1 kΩ	100 Ω	71 v	22 ma
10 kΩ	1 kΩ	71 v	14.1 ma
100 kΩ	10 kΩ	223 v	14.1 ma
1 MΩ	100 kΩ	500 v	14.1 ma
10 MΩ	1 MΩ	500 v	14.1 ma

* It is preferable to limit current to avoid shock hazard or to reduce voltage to 10 v.

TABLE 2
EIA STANDARD TEST VOLTAGES
(RS 172 - FIXED COMPOSITION RESISTORS)

Resistance Range	Bridge Mult Range	EIA Test Voltage Range	Bridge* Voltage
2.7 - 99 Ω	1 Ω	0.5 - 1 v	**
	10 Ω	0.5 - 1 v	50 - 71 v***
100 - 999 Ω	100 Ω	2.5 - 3 v	27.5 - 33 v
1000 - 9999 Ω	1 kΩ	8 - 10 v	16 - 20 v
10 - 99 kΩ	10 kΩ	24 - 30 v	26.4 - 33 v
100 kΩ up	100 kΩ	80 - 100 v	80 - 100 v
	1 MΩ	80 - 100 v	80 - 100 v

* at EXT GEN terminals
** cannot get required bridge voltage
*** limited to 71 v by bridge

3. A suggested setup for tests at these voltages is shown in Figure 10. The voltmeter here indicates the bridge voltage, and should be set as listed in Tables 2 and 3. An alternate scheme is to put the voltmeter directly across the unknown resistor, assuming that the input resistance of the voltmeter is large enough to cause no error.

Figure 10.
Circuit for Tests
at EIA Voltages.

CURRENT-LIMITING
RESISTOR
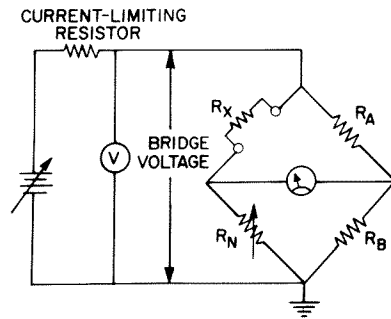
TABLE 3
EIA STANDARD TEST VOLTAGES
(RS 196 FIXED-FILM RESISTORS
REC 117 LOW-POWER WIRE-WOUND RESISTORS)

Resistance Range	Bridge Mult Range	EIA Max Test Voltage	Max Bridge Voltage*
less than 10 Ω	1 Ω	0.3 v	**
10 - 99 Ω	10 Ω	1 v	**
100 - 999 Ω	100 Ω	3 v	33 v
1000 - 9999 Ω	1 kΩ	10 v	20 v
10 - 99 kΩ	10 kΩ	30 v	33 v
100 kΩ up	100 kΩ	100 v	101 v
	1 MΩ	100 v	100 v

REC 117 applies only up to 9999 Ω.
* At EXT GEN terminals.
** Maximum allowance bridge voltage will not give maximum test voltage.

SECTION 5

OPERATING PROCEDURE——1-KC MEASUREMENTS

5.1 CAPACITANCE MEASUREMENT.

5.1.1 PROCEDURE.

a. Set OSC LEVEL control fully on (clockwise).

b. Set DETECTOR switch to 1 kc.

c. Set CRL Selector to

C_s - if series capacitance is desired and D is less than 1.

C_p - if parallel capacitance is desired and D is between 0.1 and 50.

(Note: $C_s = C_p$ within 1% if D $<$ 0.1.)

d. Set the function switch to INT 1 KC.

e. Connect the unknown capacitor to the UN-KNOWN terminals.

f. If the proper range setting of the CRL MULTIPLIER is not known, set the CRL dial at about midscale, adjust the SENSITIVITY control to give an upscale meter reading, and set the CRL MULTIPLIER switch for a minimum deflection.

g. Adjust the CRL and DQ controls for the best minimum meter reading. The SENSITIVITY control may have to be adjusted to give greater sensitivity as balance is approached.

h. The capacitance of the unknown equals the product of the CRL dial reading and the CRL MULTIPLIER switch setting. The D of the unknown is that indicated on the appropriate scale on the DQ dial.

If the D of the unknown is near or greater than 1, the Orthonull balancing mechanism is useful. Refer to paragraph 5.5.

Refer to paragraphs 7.6 and 7.8 for measurements on shielded and grounded capacitors.

5.1.2 ACCURACY. The accuracy of the C reading is ±1% if the balance is made between 1 and 11 on the CRL dial. Below 1 on the dial the accuracy is ±1/2 division. Thus the over-all accuracy possible is ±1% or ±1 pf, whichever is greater, since 1 pf is 1/2 a dial division on the lowest range. The D accuracy is ±5% or ±0.001, whichever is greater, with a CRL dial reading of 1 or higher.

The residual ("zero") capacitance of the bridge terminals is approximately 1/2 pf, which is less

than the accuracy of the bridge, and therefore, negligible. If external leads are used to connect the unknown, this zero capacitance is increased and should be subtracted from the bridge reading.

The residual resistance of the bridge is 1 milliohm, which theoretically causes a D error of 0.006 when C_x = 1000 µf. In practice, capacitors of this size have such large D values that such an error is negligible. However, if leads are used to connect large capacitors this D error may become important and a correction should be made. The D error is $+ \omega R_0 C_x$ (where R_0 is the lead resistance), and this amount should be subtracted from the D reading.

The residual inductance causes negligible error at 1 kc even if C_x = 1000 µf. However, connecting leads could have enough inductance to cause a C error when large capacitors are measured. The error is $+ \omega L_0 C_x$ (when L_0 is the lead inductance) and this amount should be subtracted from the C reading.

The capacitance accuracy is reduced on the C_p bridge when D becomes larger than 10. However, even with the Orthonull balancing mechanism, balance to 1% precision is impossible, so that this error is negligible. Refer to paragraph 2.5 and Figure 9.

Errors for capacitance measurements at other frequencies are discussed in paragraph 6.6 and 6.7. Table 5 (page 15) lists the corrections for residual and lead impedances.

5.2 INDUCTANCE MEASUREMENTS.

5.2.1 PROCEDURE.

a. Set the OSC LEVEL fully on (clockwise). Note: for some iron-cored inductors the inductance measured will depend upon the excitation level (refer to paragraph 5.4.4).

b. Set the DETECTOR switch to 1 Kc.

c. Set the CRL SELECTOR to

L_s - if series inductance is desired and Q is between 0.02 and 10.

L_p - if parallel inductance is desired and Q is greater than 1.

If Q is not known, use L_s and set the DQ dial to a high Q value.

(Note: $L_s = L_p$ within 1% if Q > 10)

d. Set the function switch to INT 1 KC.

e. Connect the inductor to be measured to the UNKNOWN terminals.

f. If the proper range setting of the CRL MULTIPLIER is not known, set the CRL dial at about midscale, set the SENSITIVITY control to give an upscale meter reading, and adjust the CRL MULTIPLIER switch for a minimum deflection.

g. Adjust the CRL control and the DQ control for the best minimum meter reading. The SENSITIVITY control may have to be adjusted to give greater sensitivity as balance is approached.

h. The inductance of the unknown inductor equals the product of the CRL dial reading and the CRL MULTIPLIER setting. The Q of the unknown is that indicated on the appropriate scale on the DQ dial.

If the Q of the unknown is near or less than 1, the Orthonull balancing mechanism is useful. Refer to paragraph 5.5.

5.2.2 ACCURACY. The accuracy of the L reading is ±1% if the balance is made between 1 and 11 on the CRL dial. Below 1 on the dial the accuracy is ±1/2 division. Thus the over-all accuracy is ±1% or ±1 µh, whichever is greater, since 1 µh is 1/2 dial division on the lowest range. The Q accuracy is given in terms of D = 1/Q and is ±5% or ±0.001, whichever is greater, with a CRL reading of 1 or higher.

The residual (zero) inductance is less than 0.2 µh, which is less than the accuracy of the bridge and therefore negligible. If external leads are used to connect to the unknown, this zero inductance is increased and should be subtracted from the bridge reading.

The residual resistance of the bridge is 1 milliohm, which causes a small D (1/Q) error. This error is less than 0.001 if L_x is more than 160 µh. If long leads are used to connect to the unknown, this error may become appreciable and require a correction. The D error is $+ \dfrac{R_0}{\omega L_x}$ (the Q error is $Q^2 \dfrac{R_0}{\omega L_x}$) where R_0 is the total lead resistance.

The residual zero capacitance of 0.5 pf theoretically causes an error for inductors above 250 henrys. However, this small capacitance is almost always negligible compared with the capacitance of the winding of such a large inductor. If the inductor is shielded, a three-terminal measurement will reduce the effect of stray capacitance to the shield (refer to paragraph 7.6). In order to reduce the effect of the winding capacitance it is necessary to reduce the measurement frequency. The inductance error due to a shunt capacitance C_0 is $\omega^2 C_0 L_x^2$, and this amount should be subtracted from the bridge reading. (Refer to Table 5.)

The inductance accuracy is reduced slightly if Q is less than 0.1. However, even with Orthonull

balance to 1%, precision is impossible, so that this error is negligible. Refer to paragraph 2.5 and Figure 9.

Errors for inductance measurements at other frequencies are discussed in paragraphs 6.6 and 6.7.

5.3 AC RESISTANCE MEASUREMENT.

5.3.1 PROCEDURE.

a. Set the OSC LEVEL control fully on (clockwise).

b. Set the DETECTOR switch to 1 kc.

c. Set the CRL SELECTOR to R.

d. Set the function switch to INT 1 KC.

e. Connect the unknown resistor.

f. If the proper range setting of the CRL MULTIPLIER is not known, set the CRL dial at about midscale, set the SENSITIVITY control to give an upscale meter reading, and set the CRL MULTIPLIER switch for a minimum deflection.

g. Adjust the CRL control for the best minimum meter reading. The SENSITIVITY control may require adjustment to give greater sensitivity as balance is approached.

h. The resistance of the unknown equals the product of the CRL dial reading and CRL MULTIPLIER switch setting.

5.3.2 ACCURACY OF AC RESISTANCE MEASUREMENTS.
The accuracy of the R reading is ±1% if the balance is made between 1 and 11 on the CRL dial. Below 1 on the dial the accuracy is ±1/2 a division. Thus the over-all accuracy is ±1% or ±1 milliohm, whichever is greater, as long as the 1-milliohm residual resistance is subtracted from the R reading.

The residual resistance of 1 milliohm is that of the binding posts themselves. For low-resistance measurements, short, heavy leads should be used as connections to the unknown. The zero resistance of the leads should be measured with the free ends connected together, and subtracted from the bridge reading with the unknown in place. The user should be particularly careful when using banana-pin connections. For best connection to the bridge, screw the binding post hard enough to notch the wire inserted in the hole.

Since there is no internal Q adjustment on the R bridge, reactance affects only the ability to get a good sharp null. If the reactance is large enough to

prevent a satisfactory balance, an external capacitor may be used to make a reactance balance (refer to paragraph 7.4).

5.4 NOTES ON AC MEASUREMENTS.

5.4.1 CAPACITANCE TO GROUND.
The Type 1650-A Bridge generally measures "ungrounded" components, since neither UNKNOWN terminal is connected directly to the panel. The panel should be connected to a good ground, especially if high-impedance components are to be measured. If the panel is not grounded, stray capacitances from the UNKNOWN terminals and panel to ground can produce an effective capacitance across the UNKNOWN terminals. With the panel grounded, capacitances from the UNKNOWN terminals to ground have a much less serious effect. (For measurements of grounded components refer to paragraph 7.8.)

The effects of stray capacitances to the panel (ground) are usually negligible in the capacitance bridges (see Figure 11). Capacitance from the LOW terminal to ground (C_a) shunts the detector and causes no error. Capacitance from the other terminal to ground (C_b) shunts the standard capacitor (C_t) and produces an error of

$$-\frac{C_b}{C_t} \times 100\% = -\frac{C_b}{0.1\,\mu f} \times 100\%$$

Since C_t is large, it takes 1000 pf to produce a 1% error (when D is small).

In the inductance bridge (see Figure 12) C_a is across the detector and has no effect, but C_b shunts the CRL rheostat. Capacitance across this rheostat

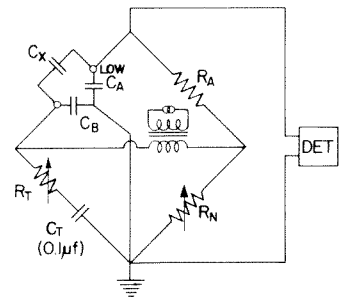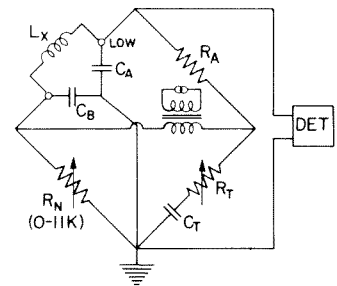Figure 11.
Capacitance to Ground
for Capacitance Measurement.

Figure 12.
Capacitance to Ground
for Inductance Measurement.

11

causes a D (1/Q) error of $-\omega R_n C_b$. The L error is usually negligible except when Q_x is very low.

$$\left[L \text{ meas} = L_x (1 + \frac{\omega R_n C_b}{Q_x}) \right]$$

Thus, for inductance measurements, it is desirable to connect the terminal with the most capacitance to ground to the UNKNOWN terminal marked LOW.

5.4.2 D AND Q ACCURACY. D (or 1/Q) accuracy is dependent upon frequency and the CRL dial setting.

CRL dial setting of 1 or above:
1-kc or lower: ±0.001 ±5%
Above 1-kc: ±0.001 (f/1 kc) ±5%
CRL dial setting below 1
1-kc or lower: ±(0.001)(1/CRL dial setting) ±5%
Above 1-kc: ±(0.001) (1/CRL dial setting) (f/1 kc) ±5%

Note that the percentage accuracy, 5%, applies directly to Q but that the fixed-accuracy term, ±0.001, does not apply directly because Q = 1/D = $\frac{1}{\pm 0.001}$ = ±1000, which is not true.

Note also that the corrections for residual and lead impedances must be taken into account (paragraph 6.5).

5.4.3 VOLTAGE ON THE UNKNOWN. The voltage applied to the bridge is approximately 1 volt, with a source impedance of about 150 ohms. The actual voltage on the unknown may be calculated with the aid of the circuit diagram of Figure 3 and Table 4, or may be measured with a high-impedance voltmeter.

TABLE 4
RATIO ARM VALUES AND VOLTAGE RATINGS

CRL MULTIPLIER			R_a Value	R_a Max Voltage	R_b Value	R_b Max Voltage
C	R	L				
100 μf	100 mΩ	100 μh	1 Ω	0.71 v	10 kΩ	71 v
10 μf	1 Ω	1 mh	10 Ω	2.2 v	10 kΩ	71 v
1 μf	10 Ω	10 mh	100 Ω	7.1 v	10 kΩ	71 v
100 nf	100 Ω	100 mh	1 kΩ	22 v	10 kΩ	71 v
10 nf	1 kΩ	1 h	10 kΩ	71 v	10 kΩ	71 v
1 nf	10 kΩ	10 h	100 kΩ	220 v	10 kΩ	71 v
100 pf	100 kΩ	100 h	1 MΩ	500 v	10 kΩ	71 v
	1 MΩ		1 MΩ	500 v	1 kΩ	22 v

5.4.4 SENSITIVITY. The generator-bridge-detector system is sensitive enough to permit 1% balances with the meter used as a detector. If higher sensitivity is required for precise measurements of D or Q at the range extremes, headphones or an external amplifier indicator, such as the GR Type 1232-A, may be connected to the DET OUTPUT terminals.

5.4.5 EFFECT OF LEVEL ON IRON-CORED INDUCTOR MEASUREMENTS. Iron-cored inductors are nonlinear devices and the value of inductance depends on the level of the applied voltage. In order to make measurements repeatable, the signal level should be specified. The "initial permeability" inductance, or inductance at zero level, is often used as a reference (as is done on GR Type 1481 Standard Inductors). To obtain this value, plot L vs voltage applied and extrapolate to zero voltage. The OSC LEVEL control permits such measurements, and it is often useful to make a level change in order to see if the unknown inductance depends on the signal level.

5.5 OPERATING PROCEDURE WITH ORTHONULL. In the measurement of inductors whose Q is less than 1 or capacitors whose D is greater than 1, balancing procedure can be simplified and false nulls avoided by the use of Orthonull (refer to paragraph 2.5). The balancing procedure (essentially the same as without Orthonull once the Orthonull mechanism is engaged) is as follows:

a. Set the bridge switches as described in paragraph 5.1.1, 5.2.1, or 6.1, depending on what is being measured. Connect the unknown to the UNKNOWN terminals, and connect the external generator (if one is used) as described in paragraph 6.2.

b. Set the Orthonull lever to ORTHONULL.

c. Set the CRL dial upscale (10 or 11).

d. Make the first balance with the DQ dial.

e. Adjust the CRL dial for further balance (the DQ dial, ganged to the CRL dial by the Orthonull mechanism, will follow). If the CRL setting is less than 1 at balance, turn the CRL MULTIPLIER switch to a lower range and rebalance.

f. Make further balances using first the DQ dial, then the CRL dial, then the DQ dial, etc. until the meter reading cannot be reduced further.

When the Q is very low, the meter deflection will give several sharp dips as the CRL dial is rotated. To find the best dip, rotate the CRL dial slowly over a wide range without making another DQ adjustment.

Often the Q is higher at some other frequency, and it is desirable to change the frequency of measurement. This is necessary if the inductor is above resonance and appears capacitive.

SECTION 6

OPERATING PROCEDURE
WITH EXTERNAL AC GENERATOR

6.1 PROCEDURE. The procedure for making measurements with an external oscillator is the same as that with the internal 1-kc oscillator except for the following:

a. Connect the external oscillator to the instrument as described in paragraph 6.2. (Note that the OSC LEVEL adjustment controls the level of external ac applied to the EXT GEN terminals.)

b. Set the DETECTOR switch to FLAT (if frequency is not 1 kc).

c. Set the function switch to AC EXT.

d. Multiply the D and Q readings by the factor M, which is given on each scale of the DQ dial.

for low D and low Q $M = f/1 \text{ kc}$

for high D and high Q $M = 1 \text{ kc}/f$

e. The accuracy of the bridge is within 1% if the value of D or Q lies within the limits of paragraph 6.4, and if the effects of the bridge residual impedance and of lead impedances are taken into account (refer to paragraph 6.5). The accuracy is 1% up to 20 kc for the C and L bridges and up to 5 kc for the resistance bridge.

If the presence of a nonlinear unknown causes appreciable distortion in the detector, the best null may not give the correct value. Earphones are helpful in distinguishing a null at the fundamental frequency, or an external selective amplifier, such as the Type 1232-A Null Detector, can be used.

6.2 CONNECTION OF EXTERNAL GENERATOR. The external generator may be connected to the bridge by any one of several methods. The choice depends on frequency and on the amount of overvoltage to be supplied.

The simplest method is to connect the generator to the EXT GEN terminals, which are connected to the primary of the bridge transformer when the function switch is set at AC EXT. Because the internal bridge transformer is used in this method, one terminal of the oscillator is tied to ground, and capacitance across the oscillator has no effect. However, the inductance of the bridge transformer primary is low (23 mh) because it is used in the internal LC oscillator, and becomes quite a load on the external oscillator at low frequencies. A resistor may be put in series with the oscillator to avoid overloading and consequent distortion. (See Figure 13a and paragraph 6.3.)

The GR Type 1311-A Audio Oscillator is recommended for this application at frequencies of 50, 60, 100, 120, 400, 500, 1000, 2000, 5000, and 10,000 cps because its output will not be distorted by overloading and it has a matching transformer to drive low-impedance loads.

A matching transformer (see Figure 13b) will provide more power in the bridge at low frequencies. This need not be a shielded bridge transformer; a filament transformer (110 to 6.3 v) is useful at low frequencies.

The external generator can also be connected directly into the bridge circuit through the BIAS terminals (be sure to open the jumper strap). See Figure 13c. In this connection capacitance from either terminal of the generator to ground should be considered. Capacitance from the + BIAS terminal to the bridge chassis causes little difficulty in the capacitance bridge if it is less than 1000 pf, but causes a Q error in the inductance bridges (refer to paragraph 5.4.1). Capacitance from the negative BIAS terminal to chassis can cause a more severe error especially at high frequencies on the low impedance ranges, and should be kept to a minimum. Use of a shielded lead (Figure 13c) keeps this capacitance low.

At times, to reduce the effects of hum between oscillator and power line, it is best to ground the oscillator and to leave the bridge chassis floating.

Figure 13. Methods of Applying External AC.

a   b   c   d
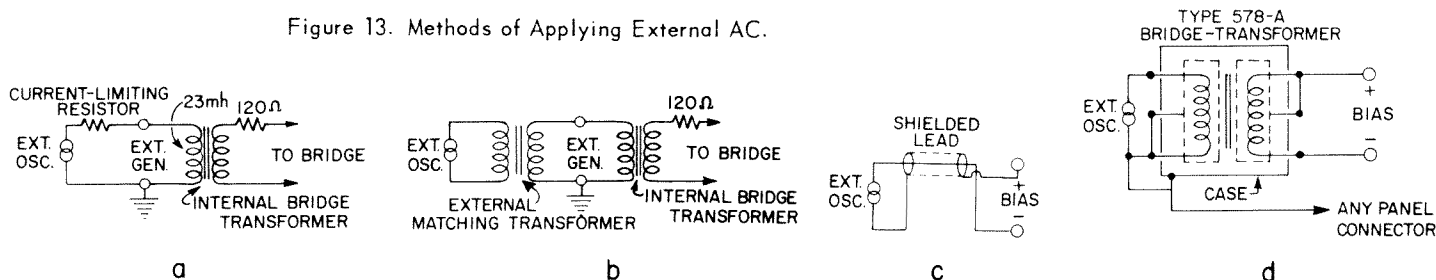
A shielded bridge transformer, such as the GR Type 578-A Shielded Transformer, may be used to make connections to the BIAS terminals to reduce capacitance difficulties. Connections are shown in Figure 13d.

6.3 MAXIMUM AC VOLTAGE. The maximum ac voltage that may be applied to the Type 1650-A Bridge depends on:

    a. the voltage and power ratings of each component (including the unknown),
    b. the bridge circuit used,
    c. the range used,
    d. the position of the variable components,
    e. the method of applying the voltage.

Exact limits for any specific measurement may be calculated from the data in Table 4 using the circuit diagrams of Figure 3. If such a maximum voltage is applied, care must be taken to avoid any adjustments of the panel controls that would result in an overload.

A much simpler approach is to limit the power into the bridge to 1/2 watt so that no bridge component can be damaged under any conditions. If the power rating of the unknown is less than 1/2 watt, the input power should be reduced accordingly.

If the external signal is applied to the EXT GEN terminals, the maximum voltage is limited to

$$E_{max} = (\frac{f}{6}) \text{ volts rms (f in cps), or}$$

60 volts (rms) whichever is smaller

With 60 volts input the maximum power to the bridge is 1/2 watt and the open-circuit secondary voltage is 15 volts.

If the external signal is connected to the BIAS terminals, the maximum voltage is 280 volts (rms), and a series resistor of $(\frac{E^2}{2} - 120)$ ohms (where E is in volts) should be placed in series to limit the power to 1/2 watt. Note that if E is 15 volts or less no resistor is required, since the resistance of the transformer secondary limits the power to the bridge.

Ac voltages up to 71 volts or up the maximum rms values listed in Table 4 under Ra Max Voltage, whichever is larger, may be applied to high impedances. Connect an external generator (a Type 1311 Audio Oscillator is recommended) between the LOW UNKNOWN terminal and ground. Connect an external detector (a Type 1232 Tuned Amplifier and Null Detector) to the EXT GEN terminals. Set the junction switch to EXT AC and the SENSITIVITY control and OSC LEVEL adjustment fully clockwise. The external generator should be current limited or power limited (1W, max) to prevent damage to the bridge in the event the unknown shorts.

6.4 ALLOWABLE D AND Q RANGES VS FREQUENCY. The D and Q readings and ranges are functions of frequency. Also, in order to avoid errors in the C and L readings, the D or Q of the unknown is further limited. The resulting allowable D and Q ranges are given in terms of frequency and D or Q of the unknown at the measurement frequency in Figure 14.

The numbers on the various limits refer to the explanations below:
    1. End of DQ rheostat range.
    2. First division on Low D (0.001) and High Q (1000) scales (no C or L error).
    3. Limited by D of standard capacitor (no C or L error).
    4. 20-cps limit because of meter response.
    5. 20 kc, a nominal limit (range narrow above 20 kc).
    6. C or L error due to capacitance across standard C_t and R_t.
    7. C or L error due to inductance in DQ potentiometer and phase of CRL potentiometer.
    8. End of the low D and high Q scales. Use the low Q scale to extend the low D range, and the high D scale to extend the high Q range.
    9. Limit of 1% C and L accuracy, even with Orthonull (refer to paragraph 2.5).
    10. C and L error may be 2% above this line owing to inductance in the DQ potentiometer.

Note that in the overlap area either the C_s or the C_p bridge may be used. Below 100 cps is an area not covered by either bridge, requiring an external adjustment (refer to paragraph 6.6).

6.5 CORRECTIONS FOR RESIDUAL AND LEAD IMPEDANCES. At high frequencies, the errors resulting from the residual bridge impedances and from the connecting lead impedances become more important, often requiring corrections. The formulas for the correction terms are given in Table 5. These correction terms are first-order terms only.

6.6 EXTENDING THE D AND Q RANGES AT LOW FREQUENCIES. The wide overlap of ranges (see Figure 14) permits D and Q coverage down to 100 cps without external adjustment. Below 1 kc, more of the low D and high Q range may be used than is calibrated. In this region, the low Q scale may be used to indicate D directly and the high D scale used to indicate Q directly with a maximum additional error of 2%.

Below 100 cps there is a D and Q range not covered by the internal DQ adjustment. An external rheostat or decade box may be used to extend the range of any of the D or Q scales. (However, to avoid error, the low D and high Q ranges should not be extended beyond a value of 1 at frequency of measurement (see Figure 14).

To connect the external resistance, remove the bridge from its cabinet and connect the two wires from the external resistance to the terminals marked 16 and 17, which are on the bracket directly behind the BIAS terminals (see Figure 21). Remove the jumper between terminals 16 and 17, and bring the
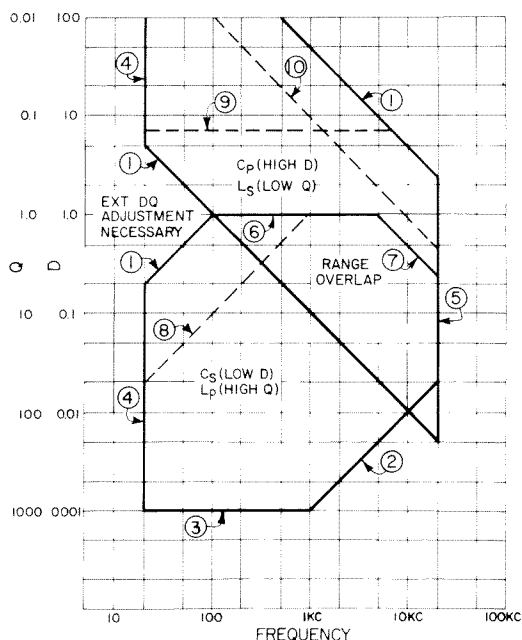
Figure 14. DQ Ranges vs Frequency.
(Refer to paragraph 6.4.)

TABLE 5
ERRORS DUE TO RESIDUAL AND LEAD IMPEDANCES
CORRECTION TERMS; ADD OR SUBTRACT
FROM MEASURED VALUE AS INDICATED

Measured Quantity	Series Resistance R_O (1 mΩ + leads)	Series Inductance L_O (0.2 μh + leads)	Parallel Capacitance C_O (0.5 pf + leads)
C_s	No Error	$-\omega^2 L_O C_x^2$	$-C_O(1-D_x^2)$
D	$-\omega C_x R_O$	$-\omega^2 L_O C_x D_x$	$+D_x \dfrac{C_O}{C_x}(1+D_x^2)$
C_p	$+2R_O\omega D_x C_x^2$	$-\omega^2 L_O C_x^2(1-D_x^2)$	$-C_O$
D	$-\omega C_x R_O(1+D_x^2)$	$-\omega^2 L_O C_x D_x(1+D_x^2)$	$+\dfrac{C_O}{C_x}D_x$
R	$-R_O$		
L_s	No Error	$-L_O$	$-\omega^2 C_O L_x^2(1-\dfrac{1}{Q_x^2})$
Q	$+Q_x^2 \dfrac{R_O}{\omega L_x}$	$-\dfrac{L_O}{L_x}Q_x$	$+\omega^2 C_O L_x(Q_x+\dfrac{1}{Q_x^2})$
L_p	$+\dfrac{2R_O}{Q\omega}$	$-L_O(1-\dfrac{1}{Q^2})$	$-\omega^2 C_O L_x^2$
Q	$+\dfrac{R_O}{\omega L_x}(1+Q^2)$	$-\dfrac{L_O}{L_x}(Q+\dfrac{1}{Q})$	$+\omega^2 C_O L_x Q$

leads out through the panel hole directly below the BIAS terminals after removing the snap button.

The low D and low Q scales are directly proportional to frequency. Therefore, the total D or Q value is the sum of the dial reading plus the ωRC product due to the external resistor. That is:

low D = (low D dial reading + 0.628R x f (kΩ, kc)

low Q = (low Q dial reading + 0.628R x f (kΩ, kc)

The low Q circuit has a fixed 32-ohm resistor in series with the potentiometer, but that is included in the dial calibration.

The high D and high Q scales are inversely proportional to frequency, and the effects of the internal and external resistors are therefore not additive. The DQ rheostat should be set to a minimum (high Q = ∞ or high D = 50), and the whole adjustment will be on the external resistance and will be:

$$\text{high } Q = \frac{1.592}{fR} \qquad (k\Omega, \text{kc})$$

$$\text{high } D = \frac{1.592}{f(R+0.032)} \qquad (k\Omega, \text{kc})$$

6.7 OPERATION ABOVE 20 KC. Although the specifications for the Type 1650-A certify performance up to only 20 kc for ac measurements, the bridge can be used with accuracy only somewhat reduced up to 100 kc. At frequencies above 20 kc, limits other than those shown in Figure 14 restrict the accuracy attainable with the bridge. These limits can be stated as a percent error, which should be added to the basic one-percent accuracy given in the instrument

specifications. The added error introduced above 20 kc is always negative, and the net effect of the two errors will probably be negative. This is shown in the following table of C_p - L_s accuracy at CRL dial settings between 0.4 and 4.

Frequency	Basic Bridge Accuracy*	Limits of Error Added Above 20 kc	Net Accuracy Limits*
50 kc	±1%	+0, -1%	+1%, -2%
100 kc	±1%	+0, -2.5%	+1%, -3.5%

*below line 10 in Figure 14

The average of the net accuracy limits shown above is -0.5% at 50 kc, -1.25% at 100 kc. If this amount is added to the measured value, the accuracy can be stated symmetrically as ±1.5% at 50 kc and ±2.25% at 100 kc.

Points to remember in measurements above 20 kc are:

a. The C_p - L_s bridges are more accurate than the C_s - L_p bridges.

b. Accuracy is greater with the CRL dial at a low setting, say between 0.4 and 4.

c. While the basic 1% bridge accuracy may be plus or minus, the error introduced above 20 kc is always minus. For greater accuracy between 50 and 100 kc, add 1% to the indicated value.

d. When measuring D or Q above 20 kc, always use the C_p - L_s bridges.

The above information is given merely as a guide for those wondering what accuracy they might reasonably expect at frequencies from 20 to 100 kc. Bridges are not tested at these frequencies, and thus operation above 20 kc is not included in the specifications.

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SECTION 7

SPECIAL MEASUREMENTS

7.1 APPLICATION OF DC BIAS TO CAPACITORS.

7.1.1 OPERATION WITH INTERNAL OSCILLATOR.
Up to 600 volts of dc bias may be applied to the unknown capacitor by any of several different methods. The simplest method can be used for measuring only series capacitance; fortunately, this is how most capacitors are specified.

WARNING

Charged capacitors form a shock hazard, and care should be taken to ensure personal safety during measurement and to be sure that the capacitors are discharged after measurement. The external dc supply should also be handled carefully.

It is advisable to limit the power that may be drawn from the external dc supply to 1/2 watt (by a resistor, fuse, or circuit breaker) in order to protect the bridge components in case the unknown is short-circuited.

The various methods of applying dc bias to capacitors are described below, along with suggestions for their use:
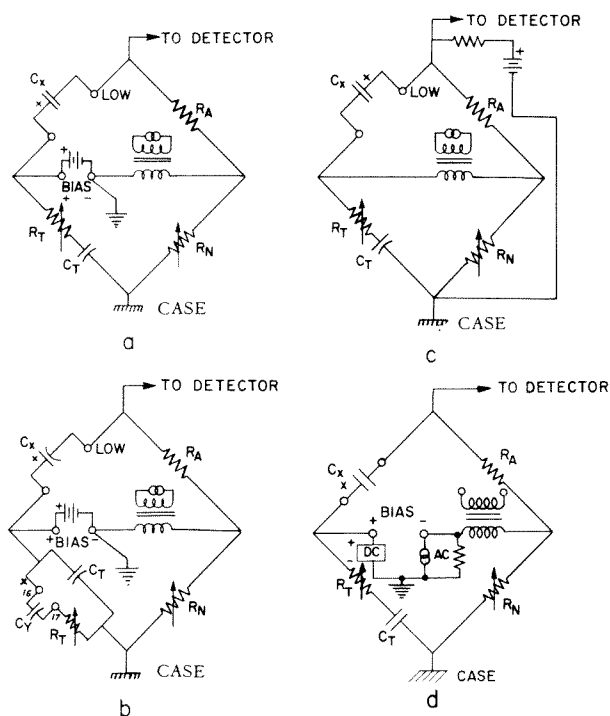

Figure 15. Methods of Applying DC Voltages to Capacitors.

Method 1. C_s Bridge (see Figure 15a).

In this method, up to 600 volts may be applied on any range. Connect the negative terminal of the unknown capacitor (if polarized) to the LOW UNKNOWN terminal. The dc supply used should have a low ac output impedance. It is usually helpful to ground the negative side of the dc supply and to leave the bridge floating to avoid hum from the power line. If the negative side of the supply (-BIAS terminal) is grounded, the bridge panel and LOW UNKNOWN terminal will be at low dc potential with low signal voltage on them.

Method 2. C_p Bridge (see Figure 15b).

The same precautions mentioned in Method 1 apply here, and a blocking capacitor should be added between the internal terminals 16 and 17, which are directly behind the BIAS terminals. The positive side of the blocking capacitor should be tied to terminal 16 as shown in Figure 15b. The voltage rating of this capacitor should be sufficient for the full dc applied. The capacitance required depends on the D of the unknown and on the accuracy required. The errors caused by this capacitor are:

$$C \text{ measured} = C_x \left(1 - \frac{C_t}{C_y} D_x^2\right) \quad \text{where } C_t = 0.1 \ \mu f$$

$$D \text{ measured} = D_x \left(1 + \frac{C_t}{C_y} D^2\right) \quad \text{and } C_y >> C_t$$

Method 3. C_s or C_p Bridge (see Figure 15c).

This method is recommended for small capacitors. The maximum voltages that may be applied to the C_s bridge are given in Table 6. For the C_p bridge, the maximum voltages on the unknown given in Table 6 apply, but the maximum voltages on the bridge are a function of the DQ dial setting.

The ac impedance of the dc source should be high (>10 k) to avoid shunting the detector, and the dc source should have low hum. The advantages of this circuit are that the bridge and supply are both grounded and the dc current can be easily limited by a resistor, since the impedance of the source should be high.

WARNING

Note that the LOW UNKNOWN terminal has the high voltage on it in this method.

TABLE 6
MAXIMUM DC VOLTAGES APPLIED
TO CAPACITORS
BY METHOD 3

Range Multiplier	Max Volts On Bridge	Max Volts On Unknown
100 pf	505 v	500 v
1 nf	242 v	220 v
10 nf	142 v	71 v
100 nf	78 v	7 v
1 µf	72 v	0.7 v
10 µf	71 v	0.07 v
100 µf	71 v	0.007 v

7.1.2 OPERATION WITH EXTERNAL AC GENERATOR.

When both external ac and dc supplies are used, hum may be introduced by the capacitance to the line in the power transformers of these generators. The bridge should be set up as shown in Figure 15d, with both the ac and dc supplies grounded and the bridge not grounded. The ac generator should be shunted by a resistor if it does not provide a path for dc.

Method 3, Paragraph 7.1.1, may also be used to apply dc bias. The bridge and both the ac and dc supplies are grounded (Figure 15c), and the ac generator is connected to the EXT GEN terminals. This method is particularly useful for high-frequency measurements of small capacitors. (Refer to Paragraphs 6.2 and 7.1.1.)

7.2 APPLICATION OF DIRECT CURRENT TO INDUCTORS.

Direct current may be supplied to inductors during measurement by any of several different methods so that incremental inductance measurements may be made. The various methods are described below along with suggestions for their use. A blocking capacitor (C_b in Figure 16) is needed only for the L_s bridge shown. This capacitor (not supplied with the bridge) should be connected by the user between terminals 16 and 17, on a bracket behind the BIAS terminals (see Figure 21). The errors caused by this capacitor are:

$$L_s \text{ measured} = L_x \left(1 - \frac{C_t}{C_b} \frac{1}{Q_x^2} \right) \qquad C_t = 0.1 \ \mu f$$

$$Q \text{ measured} = Q_x \left(1 - \frac{C_t}{C_b} \frac{1}{Q_x^2} \right)$$
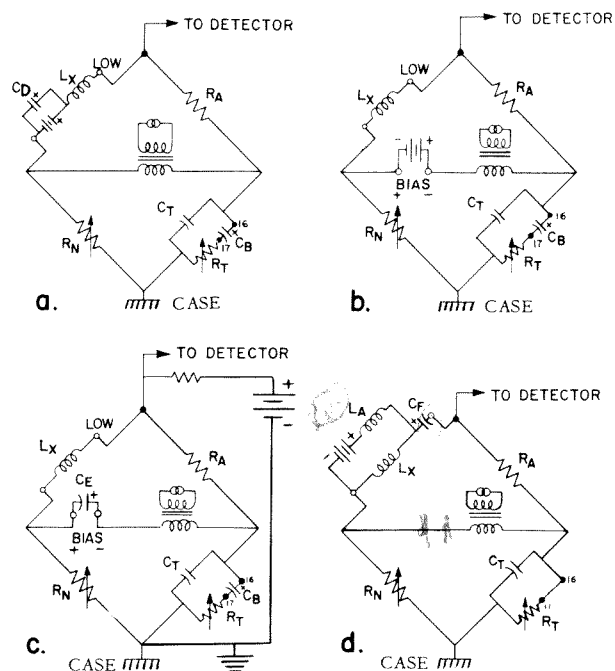
Figure 16. Methods of Applying DC to Inductors. (Blocking Capacitor C_b is not Supplied with the Bridge)

WARNING

Large inductors carrying high currents are shock hazards. Reduce the dc to zero before disconnecting the dc supply or unknown inductor.

Method 1. (See Figure 16a.)

The maximum current is limited to that given in Table 7. The dc supply may be tied to ground and the instrument left floating as shown, where the capacitance of the bridge to ground shunts R_n and causes a D (1/Q) error of $-\omega R_n C$. If the dc supply has low capacitance to ground and low internal capacitive coupling to the power line, the bridge may be grounded and the dc supply left floating.

The blocking capacitor, C_b, must be of high enough rating to take a voltage equal to the maximum direct current in amperes times 120 ohms.

The source impedance of the dc supply must be low compared with that of the unknown, since the bridge measures both of these impedances in series. A large capacitor (C_d) shunting the dc supply is sometimes useful.

Method 2. (See Figure 16b.)

The maximum current in this method is limited to that given in Table 7. The dc supply is connected to the BIAS terminals with the signs reversed in order to keep the bridge case and dc supply both

at zero volts dc from ground. The blocking capacitor C_b must be able to take the full dc voltage. The ground connection may be made to either the panel or the dc supply.

TABLE 7
MAXIMUM DC THROUGH INDUCTORS
OR RESISTORS
(METHODS 1 AND 2)

Range Multiplier		Maximum Current	R_a (Ratio Arm)
L	R		
100 μh	100 mΩ	100 ma	1 Ω
1 mh	1 Ω	100 ma	10 Ω
10 mh	10 Ω	71 ma	100 Ω
100 mh	100 Ω	22 ma	1 kΩ
1 h	1 kΩ	7.1 ma	10 kΩ
10 h	10 kΩ	2.2 ma	100 kΩ
100 h	100 kΩ	0.5 ma	1 MΩ
	1 MΩ	0.5 ma	1 MΩ

<u>Method 3.</u> (See Figure 16c.)

This method is recommended for large inductors, since the maximum current is the same for any range. In this method both the bridge and the dc supply are grounded.

The maximum allowable current for any range is 40 ma. The output impedance of the dc supply should be high enough to avoid loading the detector (a series resistor is often useful) and should have low hum.

The blocking capacitor C_e must be able to take the dc IR drop across the unknown inductor, and C_b must be able to take the whole dc voltage.

<u>Method 4.</u> (See Figure 16d.)

The method must be used with very large dc. The maximum voltage on the unknown is limited only by the rating of C_f. The ac source impedance of the dc supply must be much higher than the impedance of the unknown since the bridge measures the parallel combination of these two impedances. A large inductor, L_a, may be connected as shown to provide a high source impedance. Often it is possible to resonate the feed inductor to increase the source impedance further. Also, the impedance of the blocking capacitor, C_f, should be low compared with the impedance of the unknown since it is directly in series with the unknown. The blocking capacitor, C_b, is not needed for this method and can be shorted out or removed.

7.3 DC BIAS FOR AC RESISTANCE MEASUREMENTS. A dc bias voltage and current may be applied to various types of nonlinear resistive ele-
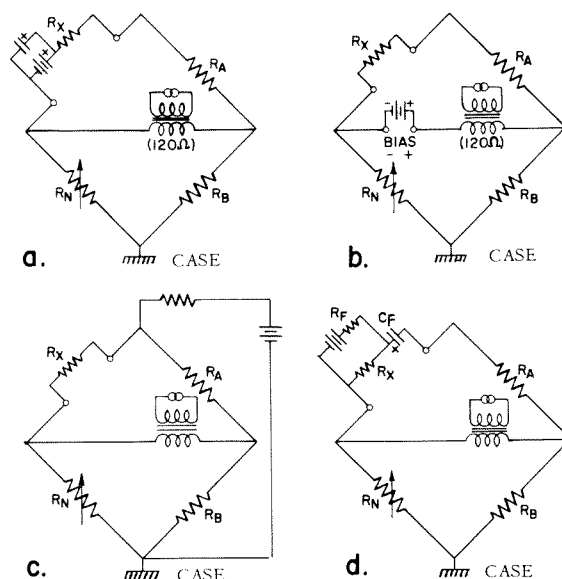
Figure 17. Methods of Applying DC for AC Resistance Measurements.

ments such as diodes, varistors, and thermistors in order to measure small ac signal resistance. For voltage-sensitive devices, diodes, and varistors, the ac resistance is the slope of the dc voltage-current curve. For thermally sensitive devices, the ac resistance is equal to the dc resistance as long as the time constant is much longer than the period of the ac signal. Several methods of applying dc are shown in Figure 17.

<u>Method 1.</u> (See Figure 17a.)

In this method all of the current supplied flows through the unknown. The current is limited to the amount given in Table 7. The dc source impedance should be low compared with that of the unknown, or the source should be shunted by a large capacitor as shown. If the dc supply is grounded, the bridge chassis may be at a potential of up to 6 volts.

<u>Method 2.</u> (See Figure 17b.)

This method removes the dc supply from the bridge arm so that its impedance is not so important. The current in the unknown is equal to the current supplied multiplied by $\dfrac{R_b}{R_a + R_b}$, and should be limited to that given in Table 7. The voltage applied should be limited to 71 volts*. If the dc supply is grounded, the bridge chassis may be at a potential of up to 37 volts.

<u>Method 3.</u> (See Figure 17c.)

This method permits grounding of both the bridge chassis and the dc supply. The current

*22 volts at 1MΩ range.

through the unknown is equal to the current supplied multiplied by $\dfrac{R_a}{R_a + R_x}$. The dc current and voltage limits are given in Table 1, page 8.

Method 4. (See Figure 17d.)

This method permits large currents through low resistors, since no current flows in the bridge. The resistor R_f should be large compared with the unknown, and the blocking capacitor, C_f, should be able to take the dc voltage $I_{dc}R_x$. The impedance of the blocking capacitor should be low compared with that of the unknown.

7.4 MEASUREMENT OF AC RESISTANCE WITH REACTANCE. If the unknown resistor has a large reactance, a good ac balance is difficult to obtain. Use of an external capacitor to balance the reactance will permit a sharp balance.

If the unknown is capacitive, the external capacitor should be connected from either BIAS terminal to ground, as in Figure 18a. At balance, the CRL dial will read the effective parallel resistance of the unknown, and the external capacitance C_n is a measure of the capacitance of the unknown. The formula is
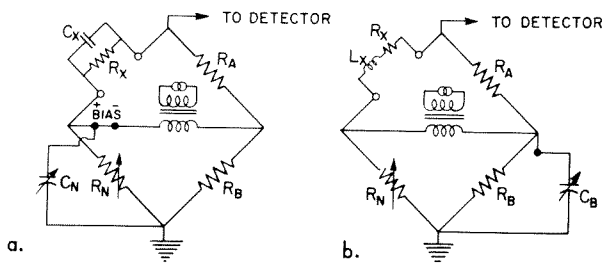
$$C_x = C_n \frac{R_n}{R_x}$$

Figure 18. Measurement of Resistance with Reactance.

If the unknown is inductive the external capacitor should be connected across the standard resistance as in Figure 18b. The connection must be made internally to terminal 16 (located on a bracket behind the BIAS terminals), and the lead brought out through the panel hole. With this connection the CRL dial indicates series resistance and the external capacitor C is a measure of the Q of the resistor. The formula is

$$Q = \omega R_b C_b$$

where $R_b = 10$ kΩ, except on the 1 MΩ range where it is 1 kΩ.

Note that R series = R parallel within 1% as long as Q is less than 0.1. The formulas are

$$R_s = R_p \frac{1}{1 + Q^2}$$

$$R_p = R_s (1 + Q^2)$$

$$Q = \frac{\omega L_s}{R_s}$$

$$Q = \omega R_p C_p$$

The reactive balances are limited to a Q accuracy of about ±0.01.

7.5 RESONANT FREQUENCY OF TUNED CIRCUITS. The resonant frequency of a series or parallel tuned circuit may be found by means of an external variable-frequency oscillator and the ac resistance bridge. The external oscillator is connected as described in paragraph 6.2, and the tuned circuit is connected to the UNKNOWN terminal.

The frequency and the CRL dial are then varied for the best null attainable. The bridge indicates, at balance, the effective series resistance of a series tuned circuit or the effective parallel resistance of a parallel tuned circuit, while the oscillator indicates the resonant frequency.

7.6 MEASUREMENTS ON SHIELDED THREE-TERMINAL COMPONENTS. When the unknown is shielded and the shield is not tied to either unknown terminal, a three-terminal component is formed (see Figure 19). The impedance Z of the component itself is the direct impedance of the three-terminal system. To measure the direct capacitance of a three-terminal system, connect the third terminal to the panel of the instrument, using any grounded panel terminal or a ground lug with screw just below the UNKNOWN terminals. The capacitances to the shield have negligible effect as long as one of them is reasonably small (refer to paragraph 5.4.1).
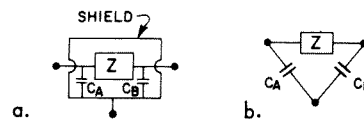
Figure 19. Shielded Three-Terminal Impedance.

Often the shield of an inductor is not connected to either terminal. When the inductance and frequency are low so that stray capacitance across the inductor causes negligible error, the shield should be connected to the UNKNOWN terminal marked LOW. When the inductance (or frequency) is high, the effective inductance is increased because of the shunting capacitance. The error is $+100\ (\omega^2 L_x C_x)\%$ (refer to paragraph 5.2.2). To avoid an inductance error, the shield may be tied to the panel of the bridge.

The inductor terminal that has the larger capacitance to the shield should be tied to the LOW bridge terminal. A Q error results from the capacitance from the other UNKNOWN terminal to the shield (C_b in Figure 12) but a better measurement of L_x is possible. (This connection does not affect the winding capacitance itself.)

7.7 REMOTE MEASUREMENTS.

Due to the small effect of stray capacitance to ground, particularly for capacitance measurements (refer to paragraph 5.4.1), the unknown may be placed some distance away from the bridge. If at least one of the connecting leads is shielded, the capacitance between the leads is avoided. The shielded lead should be connected to the LOW UNKNOWN terminal, and the bridge should be grounded. The other lead may also be shielded, at the cost of increased capacitance to ground. When low impedance measurements are made, the effect of the lead resistance and inductand should be considered (see Table 5).

7.8 MEASUREMENT OF GROUNDED COMPONENTS.

If the component to be measured is connected directly to ground, the component may be measured with the case of the Type 1650-A floating off ground.

Either unknown terminal of an unknown capacitor may be grounded. Grounding the low terminal tolerates large capacitance from the case to ground, but increases sensitivity to hum. However, most of the hum can be removed by the internal 1-kc filter in the amplifier. Grounding the other unknown terminal decreases sensitivity to hum, but a capacitance of 1000 pf from the case to ground causes a 1% capacitance error (refer to paragraph 5.4.1).

If the unknown is an inductor, the LOW terminal should be grounded.

Even when the bridge is floating, the bridge panel can be used as a guard terminal for three-terminal or remote measurements.

7.9 USE OF THE TYPE 1650-P1 TEST JIG.

7.9.1 GENERAL. The 1650-P1 Test Jig provides a means of making quick connections to the bridge with a pair of conveniently located clip terminals. When the Type 1650-A is set up for limit measurements (refer to paragraph 7.10), the combination facilitates the rapid sorting of electrical components.

The jig is also useful for measurements on small capacitors because of its small zero capacitance and because the unknown component is positioned and shielded to make repeatable measurements possible.

7.9.2 INSTALLATION. The test jig is connected to the bridge UNKNOWN terminals by means of the shielded Type 274 Connector attached to the jig. A three-terminal connection is necessary. The third connection is made by means of the screw, located directly below the UNKNOWN terminals, and the lug on the shield of the connector. This screw makes the ground connection to the jig and also holds the connector in place.

The leads of the test jig may be brought around in back of and underneath the bridge so that the jig may be located directly in front of the bridge without interference from the leads.

7.9.3 RESIDUAL IMPEDANCES OF THE TEST JIG. The residual resistance of the leads is about 80 milliohms (total) and the inductance is about 2 µh. The zero capacitance, when the leads are connected to the bridge, is negligible (≈ 0.2 pf). The shielded leads cause a capacitance to ground of about 100 pf each. Corrections may be necessary for the residual resistance and inductance when measurements are made on low impedances (see Table 5, page 15). The capacitances to ground cause no error for capacitance measurements, but can cause a D (1/Q) error up to about 0.007 for inductance measurements (refer to paragraph 5.4.1).

7.10 LIMIT TESTING.

The Type 1650-A may be set up to provide a go-no-go indication useful for component testing. The panel meter is used as the indicator. The set-up procedure is as follows:

a. Balance the bridge with one of the components to be measured (preferably one within tolerance).

b. Offset the CRL dial by the desired tolerance, if the tolerance is symmetrical, or by one half of the total allowable spread if unsymmetrical.

c. Adjust the SENSITIVITY control for a five-division meter deflection.

d. Set the CRL dial to the center value (the nominal value if the tolerance is symmetrical).

e. Connect each component to the bridge (or Type 1650-P1 Test Jig). If the meter deflection is less than five divisions, the component is within limits.

When the unknown has a tolerance greater than ±10%, the limits may be in error by more than 1% if the above method is used. A sure method is to set the CRL dial so that unknown components at both limits give the same deflection.

SECTION **8**

SERVICE AND MAINTENANCE

8.1 WARRANTY.

General Radio warrants that each new instrument sold direct is free from defects in material and workmanship, and that, properly used, it will perform in full accordance with applicable specifications for a period of two years after original shipment. Any instrument or component that is found within the two-year period not to meet these standards after examination by our factory, district office, or authorized repair agency personnel, will be repaired, or, at our option, replaced without charge, except for tubes or batteries that have given normal service.

8.2 SERVICE.

The two-year warranty stated above attests the quality of materials and workmanship in our products. When difficulties do occur, our service engineers will assist in any way possible. If the difficulty cannot be eliminated by use of the following service instructions, please write or phone our Service Department (see rear cover), giving full information of the trouble and of steps taken to remedy it. Be sure to mention the serial and type numbers of the instrument.

Before returning an instrument to General Radio for service, please write to our Service Department or nearest district office, requesting a "Returned Material Tag". Use of this tag will ensure proper handling and identification. For instruments not covered by the warranty, a purchase order should be forwarded to avoid unnecessary delay.

8.3 TROUBLE ANALYSIS.

8.3.1 PRELIMINARY CHECKS.

If satisfactory measurements are difficult or impossible to obtain, make the following external checks first:

1. Is the unknown component connected correctly?
2. Is the unknown what it is thought to be? Large inductors can look like capacitors at 1 kc.
3. Are all the panel switches set properly?
4. Is the jumper between the BIAS terminals in place?
5. Is the Q so high (D so low) that Orthonull should be used?
6. Is OSC LEVEL control on ?
7. Is SENSITIVITY control on?
8. Are the batteries correctly in place? The positive (button) end should be down.

8.3.2 TROUBLE ANALYSIS GUIDE.

The Type 1650-A Impedance Bridge incorporates five major circuits, one or more of which may be switched out by means of the function switch as follows:

TABLE 8

TROUBLE ANALYSIS GUIDE — TYPE 1650-A

Circuit	Detailed Service Information (Paragraph)	Function Switch Setting			
		EXT AC	INT 1 kC	INT 6 V	EXT DC
Oscillator	8.5.5	out (use external signal source)	in	out	out (use external power supply)
Detector	8.5.6	in	in	out	out (use external power supply)
Batteries	8.4.1	in	in	in	out (use external power supply)
Meter	8.3.3	in (external indicator may be used)	in	in	in
Bridge	8.3.5, 8.5.3 (DQ dial), 8.5.4 (CRL dial), 8.5.7	in	in	in	in

Note that an equivalent, external circuit can be substituted for all of the major circuits, except the bridge circuit.

8.3.3 NO METER INDICATION.

No meter indication, or a low meter indication, may be due to weak or dead battteries low oscillator output, poor detector sensitivity, or a faulty meter. If the trouble persists in the INT 6 V position of the function switch (where the oscillator and detector circuits are switched out), the faulty circuit is either the batteries or the meter.

The batteries can be checked either by replacement or by substitution of an external dc power supply with the function switch set to EXT DC. If the trouble persists, the meter is faulty.

The meter can be checked by connection of an external indicator (earphones, ac meter, oscilloscope, etc) to the DET OUTPUT terminals.

8.3.4 NOISY OR ERRATIC BALANCES.

Noisy or erratic balances may be due to surface contamination of the wire-wound CRL and DQ control rheostats. Contamination can form if the Type 1650-A Impedance Bridge is idle for an extended period and can be remedied by rotating the controls several times.

8.3.5 MEASUREMENT ERRORS.

Measurement errors are due to faulty bridge-circuit components which can be located with the series of ten measurements listed in Table 9. Four standard resistors and one standard capacitor are needed for these measurements.

1. When any one measurement is in error, the faulty component is listed in Table 9.
2. When both 1-megohm measurements are in error, the series combination of R12 and R13 is out of tolerance.
3. When all resistance measurements (or all except 1MΩ) are in error, R4 plus the resistance of L1 is out of tolerance.

4. When both capacitance measurements are in error, C1 is out of tolerance.
5. When all measurements are in error, the CRL rheostat is in error.
6. When all measurements at either 1 or 10 on the CRL dial are in error, the CRL rheostat is in error at either 1 or 10.
7. When all measurements are within tolerance all the fixed components of the bridge are within tolerance, and the CRL rheostat is correct at the 1 and 10 settings, the CRL rheostat still may be incorrect between 1 and 10.

8.4 REPAIR NOTES.

8.4.1 BATTERY REPLACEMENT.

The Type 1650-A Impedance Bridge is powered by four D cells, which will last for over 500 hours' operation with normal use. The instrument can operate with greatly reduced battery voltage, but the detector sensitivity will decrease and the oscillator frequency may change slightly.

For a quick check of the battery (other than simple replacement), connect an ammeter from the LOW UNKNOWN terminal to any panel (ground) terminal and measure the current flowing when the function switch is in the INT 6 V position. If this current is less than 40 ma, the cells should be replaced.

To replace the cells, remove the round cover on top of the case, remove the old cells, and insert the new ones with the positive (button) end down.

The battery in the Type 1650-A may be replaced with the rechargeable battery assembly (Type 1650-P2 Battery and Charger for 115-volt operation, Type 1650-P2Q18 Battery and Charger for 230-volt operation) supplied with relay-rack models (refer to paragraph 3.2).

TABLE 9

MEASUREMENTS FOR CALIBRATION CHECK

Measure-ment	Standard	General Radio Type No.	Bridge Circuit	Range Multiplier Setting	Faulty Component (Result 1)
A	1 Ω	500-A	R_{AC}	100 mΩ	R5
B	1 Ω	500-A	R_{AC}	1 Ω	R6
C	100 Ω	500-D	R_{AC}	10 Ω	R7
D	100 Ω	500-D	R_{AC}	100 Ω	R8
E	10 kΩ	500-J	R_{AC}	1 kΩ	R10
F	10 kΩ	500-J	R_{AC}	10 kΩ	R11
G	1 MΩ	500-X	R_{AC}	100 kΩ	(both R14 and R12 + R13)
H	1 MΩ	500-X	R_{AC}	1 MΩ	R14
I	0.1 μf	505-T	C_S	100 nf	(both C1 and R9)
J	0.1 μf	505-T	C_S	1 μf	R9

8.4.2 ETCHED CIRCUIT REMOVAL.

For access to the etched circuit shown in Figure 20, unfasten the DETECTOR switch and SENSITIVITY control. Disconnect the DET OUTPUT terminal, remove the three screws holding the board in place, and slide it out.

8.5 CALIBRATION PROCEDURE.

The few internal adjustments are factory set and normally do not require readjustment. Procedures for readjustment are included here, but should be used only when the operator is reasonably certain that readjustment is necessary.

8.5.1 EQUIPMENT REQUIRED.

The following equipment is required for complete calibration of the Type 1650-A:

Impedance Bridge -- capable of measuring 30Ω to $16k\Omega$ with an accuracy of $\pm0.3\%$, or better. The Type 1608-A Impedance Bridge can be used.

Audio Signal Source -- capable of producing 900- to 1100-cycle signals and 4-kc signals with an accuracy of ±2 cps and an amplitude of 3 volts, rms. The Type 1107-A Interpolation Oscillator can be used.

Frequency Meter -- capable of measuring the frequency of 900- to 1100-cycle signals and 4-kc signals with an accuracy of ±2 cps. A direct frequency-measuring instrument can be used, such as a Type 1150-B, 1151-A, 1130-A, or 1142-A. A frequency-comparison instrument consisting of an audio-signal source and a mixer-indicator arrangement, can be used also. The mixer-indicator can be either a heterodyne mixer and zero-beat detector (meter, earphones, etc) or an oscilloscope (Lissajous technique). The Type 1107 Interpolation Oscillator can be used.

Electronic Voltmeter -- with an input resistance of 20 kΩ, or higher, and capable of measuring, to within 2% or better, signals with frequencies of 900 cps to 4 kc and amplitudes of 0 to 5 v, rms. The Type 1806-A Electronic Voltmeter can be used.

Distortion and Noise Meter -- capable of measuring 0 to 10% second- and third-harmonic distortion, with an accuracy of $\pm5\%$, of signals with frequencies between 900 and 1100 cps and amplitudes between 1.5 and 5 v; capable of measuring 0 to -58 dbm noise (0.775 to 0.001 v, rms) with an accuracy of $\pm5\%$. The Type 1932-A Distortion and Noise Meter can be used.

Dc Power Supply -- capable of delivering 200 v, $\pm20\%$, at 3 ma. The Type 1205-B Adjustable Regulated Power Supply can be used.

Standard Capacitors

GR Type	Value	Accuracy
505-F	.001 μf	±0.5%
505-L	.01 μf	±0.5%
505-T	.1 μf	±0.5%

Standard Inductor

1481-G	100 mh	±0.4%

Standard Resistors

500-A	1 Ω	±0.15%
500-B	10 Ω	±0.05%
500-D	100 Ω	±0.05%
500-H	1 kΩ	±0.05%
500-J	10 kΩ	±0.05%
500-U	100 kΩ	±0.05%
500-X	1 MΩ	±0.05%

8.5.2 ORTHONULL OPERATION.

With the lever in the NORMAL position, the CRL and DQ dials must operate independently of each other.

With the lever in the ORTHONULL position, the CRL dial must move the DQ dial but the DQ dial must not move the CRL dial; if performance is different and ——

1. DQ dial moves CRL dial:
   ORTHONULL lever-spring tension is excessive. Turn the nut on the spade-lug counterclockwise to reduce tension.
2. CRL dial doesn't move DQ dial:
   a. ORTHONULL lever-spring tension is insufficient. Turn the nut on the spade-lug clockwise to increase tension.
   b. Lever spring is broken or otherwise defective.
   c. Drive cable between CRL dial and DQ dial is broken or off the pulley.

To replace the ORTHONULL drive cable, proceed as follows (see Figure 23):

a. Insert the cable ends through slots 1 and 2 of the DQ pulley and attach the eyelets to the springs. Note that the cable is attached only to the DQ pulley at this time.

b. Pull the cable until the eyelets are visible through holes A and B. Insert a pin or small nail through the holes into the respective cable eyelet and release the cable. The pins hold the springs expanded to allow the cable to be threaded around the CRL pulley.

c. Set the CRL dial to 1.8. Thread the cable from slot 1, around the DQ pulley in the groove nearest the panel and then around the CRL dial in the second groove from the panel.
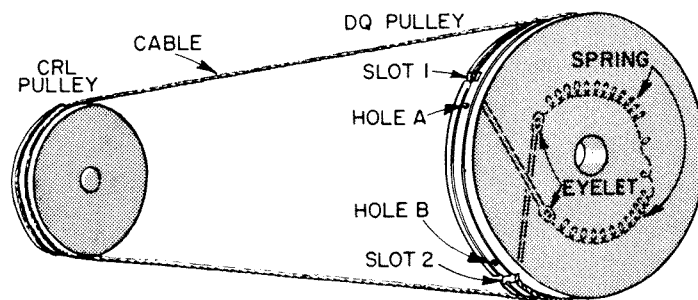

Figure 23. Replacement of ORTHONULL drive cable. For details, refer to the paragraphs below.

d. Continue the cable around the CRL pulley until it emerges from the third groove from the panel and return it to the DQ dial.

e. The cable is now completely threaded and the pins can be removed from holes A and B.

8.5.3 DQ DIAL.

Set the function switch to OFF, the CRL SELECTOR switch to Cp, and the DQ dial fully counterclockwise (50 on HIGH D scale). Connect a dc resistance bridge between ground and either one of the BIAS terminals. This setup allows the bridge to measure only the resistance of the DQ rheostat in series with R3. With the DQ dial fully counterclockwise, its resistance is zero and the bridge measures only the resistance of R3, which should be 32 Ω, ±0.96 Ω (±3%), after allowance for bridge lead resistance. If the indication is abnormal and:

1. Resistance is too high -- R3 is open or its value is too high.
2. Resistance is too low -- R3 value is too low or C1 is leaky.

Set the DQ dial to 20 on HIGH D scale. The resistance should be 79.5 Ω. If necessary, reposition the DQ dial on its shaft until the resistance is 79.5 Ω at a setting of 20. Then check the DQ dial calibration as given in Table 10.

TABLE 10

DQ DIAL CALIBRATION

DQ Dial (HIGH D)	Resistance (Ohms)	Tolerance (±3%) (Ohms)	
50	32.00	31.04 to	32.96
20	79.50	slip DQ dial for exact reading	
10	159.20	154.40 to	163.98
5	318.40	308.90 to	327.90
2	796.00	772.10 to	819.90
1	1,592.00	1,544.00 to	1,640.00
0.5	3,184.00	3,089.00 to	3,279.00
0.2	7,960.00	7,721.00 to	8,199.00
0.1	15,920.00	15,440.00 to	16,400.00

Resistance is either too high or too low:
(1) DQ rheostat is out of tolerance.
(2) C3 or C8 is leaky.

8.5.4 CRL DIAL CHECK.

Keep the function switch set to OFF, but change the CRL SELECTOR switch to Lp. This connects the CRL rheostat between ground and the BIAS terminals. The resistance measured should equal the setting of the CRL dial in kilohms, as follows:

TABLE 11

CRL DIAL CALIBRATION ADJUSTMENTS

Dial Reading	Resistance (Ohms)	Tolerance (Ohms)			Cam Screw Adjust
3.6	3,600	3,564	to	3,636 (±1%)	5
2.2	2,200	2,178	to	2,222 (±1%)	4
1.3	1,300	1,287	to	1,313 (±1%)	3
.6	600	595	to	605 (±1/4 div)	2
.1	100	95	to	105 (±1/4 div)	1
5.5	5,500	5,445	to	5,555 (±1%)	6
8	8,000	7,920	to	8,080 (±1%)	7
11	11,000	10,890	to	11,110 (±1%)	8

If the resistances are within tolerance and the dials operate properly, disconnect the resistance bridge and proceed to paragraph 8.6.5.

CRL justifying mechanism. If the readings are abnormal, the CRL rheostat mechanical justifying mechanism must be readjusted. The CRL rheostat mechanical justifying mechanism consists of eight cam screws located on the rear of the CRL rheostat (see Figure 21), numbered from 1 to 8 in a clockwise direction from the slit on the cam plate. They are adjusted by setting them for the proper resistances as indicated in Table 11.

NOTE

If these cam screws seem to be completely out of adjustment, preset cam screw 1 four turns from fully clockwise and preset the remaining screws two turns from fully clockwise before attempting the adjustment procedure.

If, after adjustment, the cam plate is too high or too low, reposition the CRL dial on its shaft and repeat the cam-screw adjustment procedure.

Slow-motion knob. If the CRL slow-motion knob slips or is inoperative, either the large disk behind the CRL dial is not riding properly in the grooved disk behind the vernier knob or there is insufficient friction between the two disks.

Loosen the nut under the small knob and move it away from the large knob to reseat the two disks or move it closer to the large knob to increase friction.

8.5.5 OSCILLATOR.

Connect power to the Type 1650-A by installing it in its cabinet or by connecting the negative side of a 6-volt battery to ground and the positive side to the anchor terminal under the CRL rheostat.

Set the function switch to INT 1 kC and the OSC LEVEL control to its mid-position. Perform the following checks by measurement of the output of the oscillator between the collector of TR 1 (test point 3) and ground:
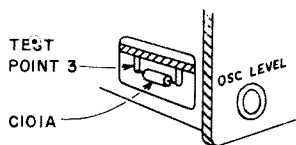
TEST POINT 3 —
OSC LEVEL
C101A —

TABLE 12

OSCILLATOR PERFORMANCE CHECK

Measurement (Test Point 3 to Ground)	Remarks
Frequency: 1000 cps, ±20 cps	Must remain within tolerance over full range of OSC LEVEL. If frequency is incorrect, change the value of C101A.
Output voltage: at least 3.3 volts, rms	Measure with OSC LEVEL fully clockwise.
Distortion: less than 5%	

NOTE

Low output or improper frequency may be due to weak batteries (refer to paragraph 8.4).

If operation is found to be abnormal, perform a stage-by-stage voltage check of the transistors (refer to Table 13).

TABLE 13

TRANSISTOR VOLTAGES

	Collector	Base	Emitter
TR 1	0	+ 5.65	+ 5.7
TR 2	+ 1.60	+ 1.05	+ 0.95
TR 3	+ 1.70	1.06	+ 0.96
TR 4	+ 6.0	+ 1.60	+1.40

Set: SENSITIVITY counterclockwise
OSCILLATOR LEVEL clockwise
INT 1 KC
A General Radio Type 1806-A Electronic Voltmeter was used to obtain the above voltages.

8.5.6 DETECTOR.

To check the detector, proceed as follows:
a. Install the Type 1650-A in its cabinet and set the controls as follows:

CRL SELECTOR	C_s
Function	AC EXT
CRL MULTIPLIER	100µF
DQ	fully clockwise
CRL	fully counterclockwise
DETECTOR	FLAT
OSC LEVEL	fully clockwise
SENSITIVITY	fully clockwise

b. Zero the NULL meter. Connect a signal source, set for 1 volt rms at 1 kc, to the EXT GEN terminals of the Type 1650-A. Connect a noise meter to the DET OUTPUT terminals of the Type 1650-A.

c. Check the NULL meter for at least three divisions of deflection. Change the DETECTOR switch to 1 kc. The NULL meter reading must not change more than one division.

d. Disconnect the oscillator and short the EXT GEN terminals together. The noise must be less than -41 db (7 mv). Disconnect the noise meter.

If the NULL meter changes more than one division between the FLAT and 1 kC positions of the DETECTOR switch, TR 3 is probably faulty. Refer to Table 13 for correct voltage levels.

8.5.7 FINAL ACCURACY AND OPERATIONAL CHECK.

The 18 measurements given in Table 14 are designed to:
1. Check the accuracy of the ratio resistors, R4 to R14.
2. Check the continuity and proper operation of the CRL SELECTOR, function, and CRL MULTIPLIER switches and the EXT GEN and UNKNOWN terminals.
3. Recheck the accuracy of the DQ and CRL dials.

Trouble-shooting notes:
1. Since the DQ and CRL dials have been checked incorrect readings on any range will ordinarily be caused by the ratio resistor, R_A, for that range (R4 to R14).
2. On the 100 mΩ range, the physical position of L1 can affect the reading as much as three divisions.
3. If R INT 6 V readings are incorrect, be sure the NULL meter is zeroed.
4. On the 100-pF and 1-nF ranges, D readings are affected by C5, R11, and R13. In most cases, the D readings on these two ranges can be brought to within 1/3 division of each other by physically moving R11 relative to R13.

In earlier instruments, C5 was variable. Its adjustment had little or no effect, however, and it was later replaced by a fixed capacitor. If C5 is variable in your instrument, it can be adjusted by setting it for D readings within 1/3 division on the 100-pF and 1-nF ranges. An alternate method is to adjust it for a zero D reading when a 0.001-µf, 3-terminal air capacitor is measured on the 100-pF range.

TABLE 14

ACCURACY AND OPERATIONAL CHECKS

EXTERNAL STANDARD GR Type	Value	CRL SEL.	Function Switch	CRL MULT.	Read	Bridge Components in Circuit R_A	R_B or R_T, C_T	R_N
500-A	1 Ω	R	INT 1 kC	100 mΩ	R = 10, ±1 div	R5, C6	L1, C2, R4	CRL
500-A	1 Ω	R	INT 1 kC	1 Ω	R = 1, ±1/2 div	R6 R5, C6	L1, C2, R4	CRL
500-B	10 Ω	R	INT 1 kC	10 Ω	R = 1, ±1/2 div	R7 R5, C6	L1, C2, R4	CRL
500-D	100 Ω	R	INT 1 kC	100 Ω	R = 1, ±1/2 div	R8 R5, C6	L1, C2, R4	CRL
500-H	1 k	R	INT 6 V	1 kΩ	R = 1, ±1/2 div	R10	L1, C2, R4	CRL
500-J	10 k	R	INT 6 V	10 kΩ	R = 1, ±1/2 div	R11	L1, C2, R4	CRL
500-U	100 k	R	*EXT 200 v	100 kΩ	R = 1, ±1/2 div	R12, R13, C5	L1, C2, R4	CRL
500-X	1 M	R	*EXT 200 v	1 MΩ	R = 1, ±1/2 div	R12, R13, C5	R14 L1, C2, R4	CRL
500-X 1 M in parallel with 505-F .001 µf		Cs	INT 1 kC	100 pF	C = 10.3, ±1/2 div D = note reading (about 0.159)	R5, C6	DQ, C1	CRL
500-X 1 M in parallel with 505-F .001 µf		Cs	INT 1 kC	1 nF	C = 1, ±1/2 div D = must be within 1/3 div of reading above	R6 R5, C6	DQ, C1	CRL
505-F	.001 µf	Cs	**EXT 1 kc	100 pF	C = 10, ±1 div	R5, C6	DQ, C1	CRL
505-F	.001 µf	Cs	**EXT 1 kc	1 nF	C = 1, ±1/2 div	R6 R5, C6	DQ, C1	CRL
505-L	.01 µf	Cs	**EXT 1 kc	10 nF	C = 1, ±1/2 div	R7 R5, C6	DQ, C1	CRL
505-T	.1 µf	Cs	**EXT 1 kc	100 nF	C = 1, ±1/2 div	R9	DQ, C1	CRL
505-T .1 µf in series with 500-D 100 Ω		Cs	**EXT 4 kc	100 nF	C = 1, ±1 div D = 0.251, ±3/4 div	R9	DQ, C1	CRL
505-T .1 µf in parallel with 500-D 100 Ω		Cp	**EXT 4 kc	100 nF	C = 1, ±1 div D = 3.98, ±3/4 div	R9	R3 DQ, C1	CRL
1481-G	100 mH	Ls	INT 1 kC	100 mH	L = 1, ±1/2 div	R8 R5, C6	R3 DQ, C1	CRL
1481-G	100 mH	Lp	INT 1 kC	100 mH	L = 1, ±1/2 div	R8 R5, C6	DQ, C1	CRL

* From external power supply.
** From external signal source.

RI08 CI07 TR2 RI16 CII6 RI19 RI02 RIOI
RI05 RI06 RI07 DIO2 RII7 CII4 RI03
CI05 RIII
RI09

TRI
CII2
CI04
CI02
CIOI
CIOI-A
RII8

CI09 CIIO CI08 TR3 CII3
RIIO CI06 RII3 RII4 RII2 RI20 CIO3
TR4 RII5
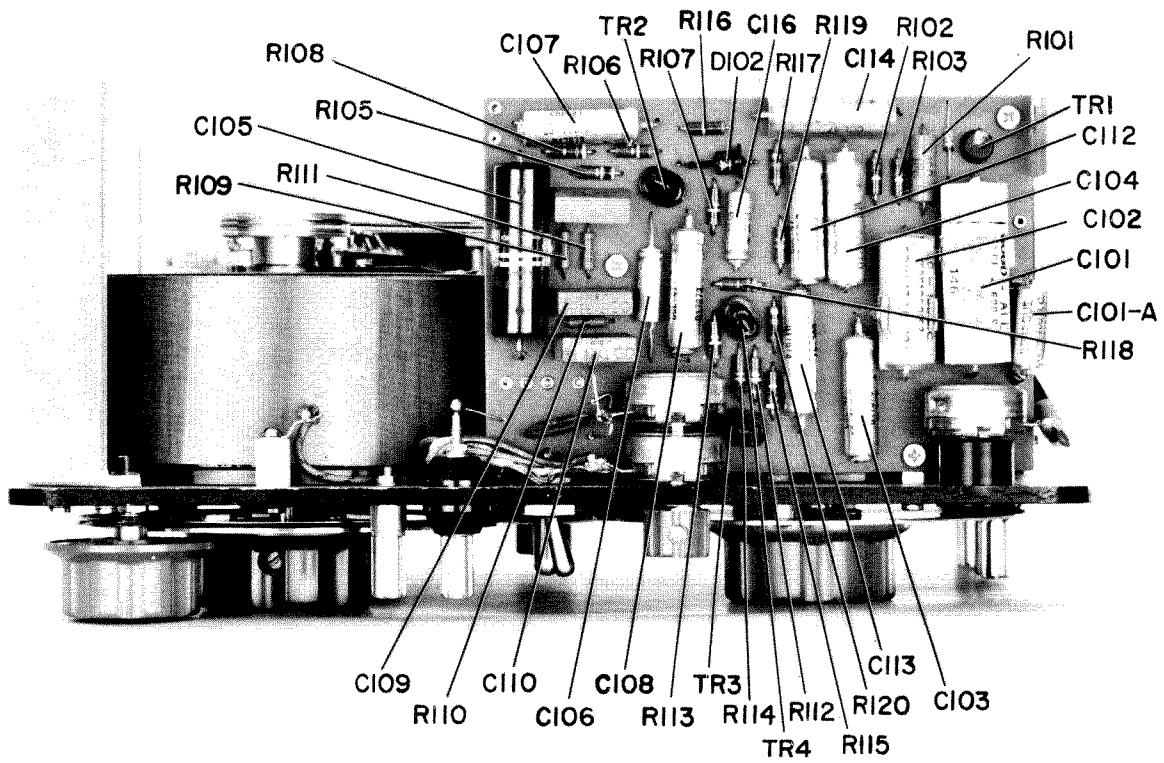
Figure 20. Bottom Interior View.
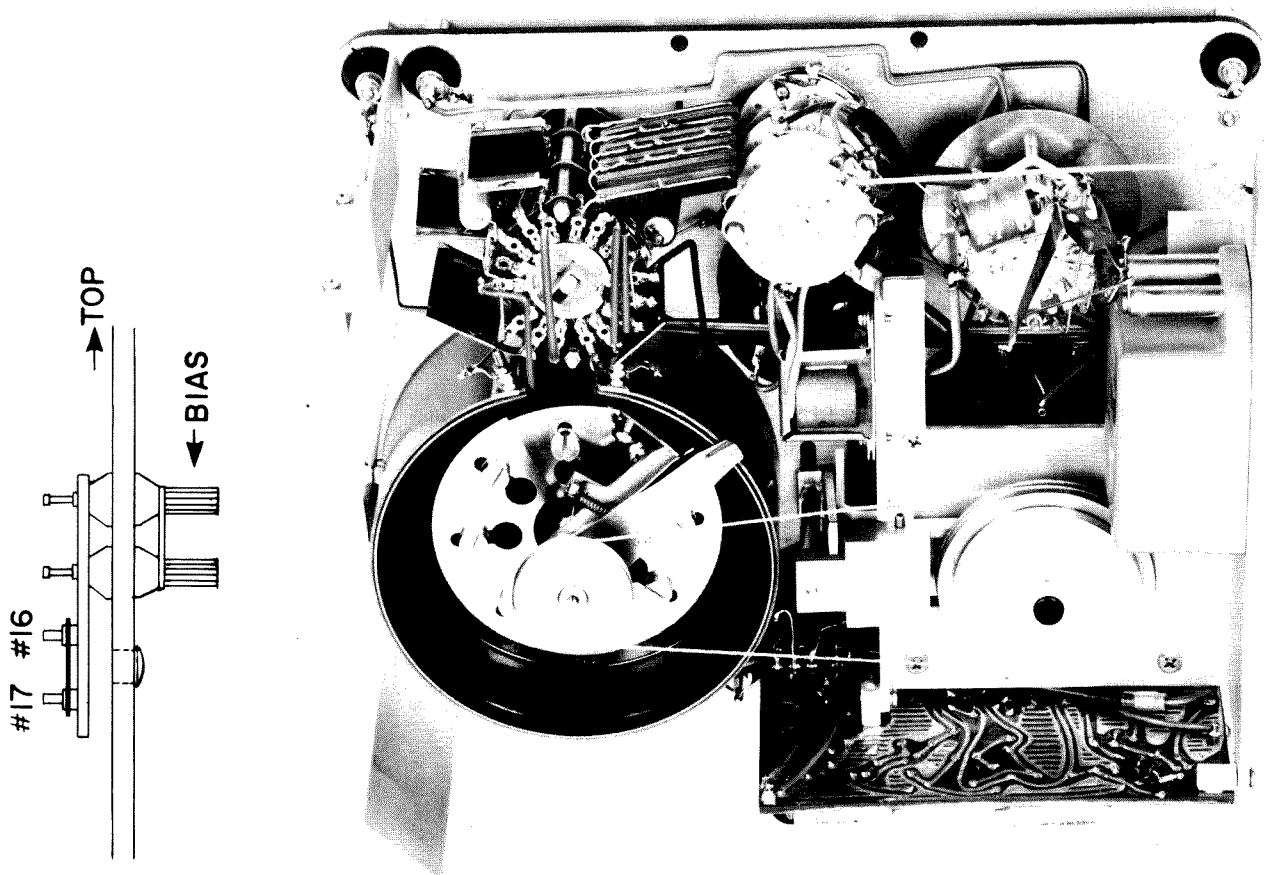
Figure 21. Rear Interior View.

→TOP

←BIAS

#17 #16

PARTS LIST

BATTERIES

B1	Type 1650-A: four Type D, 1.5-V dry cells	8410-0200
	Type 1650-9540: 115-V Rechargeable	1650-9602
	Type 1650-9570: 230-V Rechargeable	1650-9568

CAPACITORS

C1	Mica, 0.0995 - 0.0999 µF	0505-4990
C2	Mica, 220 pF ±2% 500 V	4690-2700
C3	Mica, 0.0056 µF ±10% 500 V	4720-1056
C4	Mica, 180 pF ±5% 500 V	4680-1800
C5	Ceramic, 5.6 pF ±10%	4400-0569
C6	Plastic, 0.68 µF ±10% 100 V	4860-8008
C7	Mica, 47 pF ±10% 500 V	4620-0700
C8	Wax, 0.022 µF ±10% 100 V	5010-1800
C9	Ceramic, 0.0047 µF ±10% 500 V	4407-2478
C101	Plastic, 1.0 µF ±2% 200 V	4860-8276
C101A	Supplied by Laboratory	
C102	Wax, 1.0 µF ±10% 100 V	5010-3700
C103	Electrolytic, 60 µF 25 V	4450-2900
C104	Electrolytic, 60 µF 25 V	4450-2900
C105	Oil, 0.1 µF ±10% 600 V	4510-4500
C106	Electrolytic, 5 µF 50 V	4450-3900
C107	Electrolytic, 60 µF 25 V	4450-2900
C108	Electrolytic, 60 µF 25 V	4450-2900
C109	Mica, 0.01 µF ±1% 500 V	4560-0300
C110	Mica, 0.01 µF ±1% 500 V	4560-0300
C111	Mica, 0.02 µF ±1% 300 V	4560-0400
C112	Electrolytic, 60 µF 25 V	4450-2900
C113	Electrolytic, 60 µF 25 V	4450-2900
C114	Electrolytic, 60 µF 25 V	4450-2900
C115	Ceramic, 0.022 µF ±20% 500 V	4407-3229
C116	Electrolytic, 5 µF 50 V	4450-3900

DIODE

D102	Type 1N91	6081-1009

JACKS

J1	Binding Post, Insulated, EXT GEN	4060-0100
J2	Binding Post, Uninsulated, EXT GEN	4060-1800
J3	Binding Post, BIAS +	4060-0100
J4	Binding Post, BIAS -	4060-0100
J5	Binding Post, Insulated, DET OUTPUT	4060-0100
J6	Binding Post, Uninsulated, DET OUTPUT	4060-1800
J7	Binding Post, UNKNOWN, Low	4060-0100
J8	Binding Post, UNKNOWN	4060-0100

INDUCTOR

L1	Air Choke, 45 mH ±5%	4290-3700

METER

M1	250 µa - 0 - 250 µa, 75 Ω ±5%	5730-0860

RESISTORS

R1	Potentiometer, 0 - 11.7 kΩ	0433-4080
R2	Potentiometer, 0 - 16 kΩ	0977-4020
R3	Film, 31.6 Ω ±1% 1/4 W	6350-9316
R4	Wire-wound, 9930 Ω ±1/4%	0510-3920
R5	} 0.982 Ω	
R6	} Wire-wound, 9.0 Ω ±1/4%	0510-4370
R7	} 90 Ω	
R8	} 900 Ω	
R9	Wire-wound, 1 kΩ ±0.25%	0602-3040
R10	Wire-wound, 10 kΩ ±0.25%	0602-3052
R11	Wire-wound, 100 kΩ ±1/4%	0510-3600
R12	Composition, 30 kΩ ±5% 1/2 W	6100-3305
R13	Film, 970 kΩ ±1/4% 1 W	6194-3970
R14	Wire-wound, 1.111 kΩ ±1/4%	0510-3921
R15	} Potentiometer, 500 Ω	1650-4000
R16	} 50 kΩ	
R17	Composition, 100 Ω ±5% 1/2 W	6100-1105
R18	Potentiometer, 2.5 kΩ ±10%	6030-0200
R19	Wire-wound, 4.7 Ω ±10% 1z2 W	6760-9479
R20	Composition, 470 Ω ±5% 1/2 W	6100-1475
R101	Film, 10 Ω ±1% 1/2 W	6450-9100
R102	Composition, 22 Ω ±5% 1/2 W	6100-0225
R103	Composition, 2 kΩ ±5% 1/2 W	6100-2205
R104	Composition, 150 Ω ±5% 1/2 W	6100-1155
R105	Composition, 47 Ω ±5% 1/2 W	6100-3475
R106	Composition, 120 kΩ ±5% 1/2 W	6100-4125
R107	Composition, 20 kΩ ±5% 1/2 W	6100-3205
R108	Composition, 4.7 kΩ ±5% 1/2 W	6100-2475
R109	Film, 18 kΩ ±1% 1/8 W	6250-2180
R110	Film, 18 kΩ ±1% 1/8 W	6250-2180
R111	Film, 5.75 kΩ ±1% 1/8 W	6250-1575
R112	Composition, 47 kΩ ±5% 1/2 W	6100-3475
R113	Composition, 120 kΩ ±5% 1/2 W	6100-4125
R114	Composition, 20 kΩ ±5% 1/2 W	6100-3205
R115	Composition, 4.7 kΩ ±5% 1/2 W	6100-2475
R116	Composition, 1.5 kΩ ±5% 1/2 W	6100-2155
R117	Composition, 1 kΩ ±5% 1/2 W	6100-2105
R118	Composition, 1.5 kΩ ±5% 1/2 W	6100-2155
R119	Composition, 220 Ω ±5% 1/2 W	6100-1225
R120	Composition, 1 kΩ ±5% 1/2 W	6100-2105
R121	Composition, 12 Ω ±5% 1/2 W	6100-0125

SWITCHES

S1	Rotary Wafer, CRL MULTIPLIER	7890-1720
S2	Rotary Wafer, CRL SELECTOR	7890-1730
S3	Rotary Wafer, Function	7890-1740
S4	Toggle, DETECTOR	7910-1310

TRANSFORMER

T1		0746-4320

TRANSISTORS

TR1	Type 2N1415	8210-1415
TR2	Type 2N2714	8210-1047
TR3	Type 2N2714	8210-1047
TR4	Type 2N2714	8210-1047

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